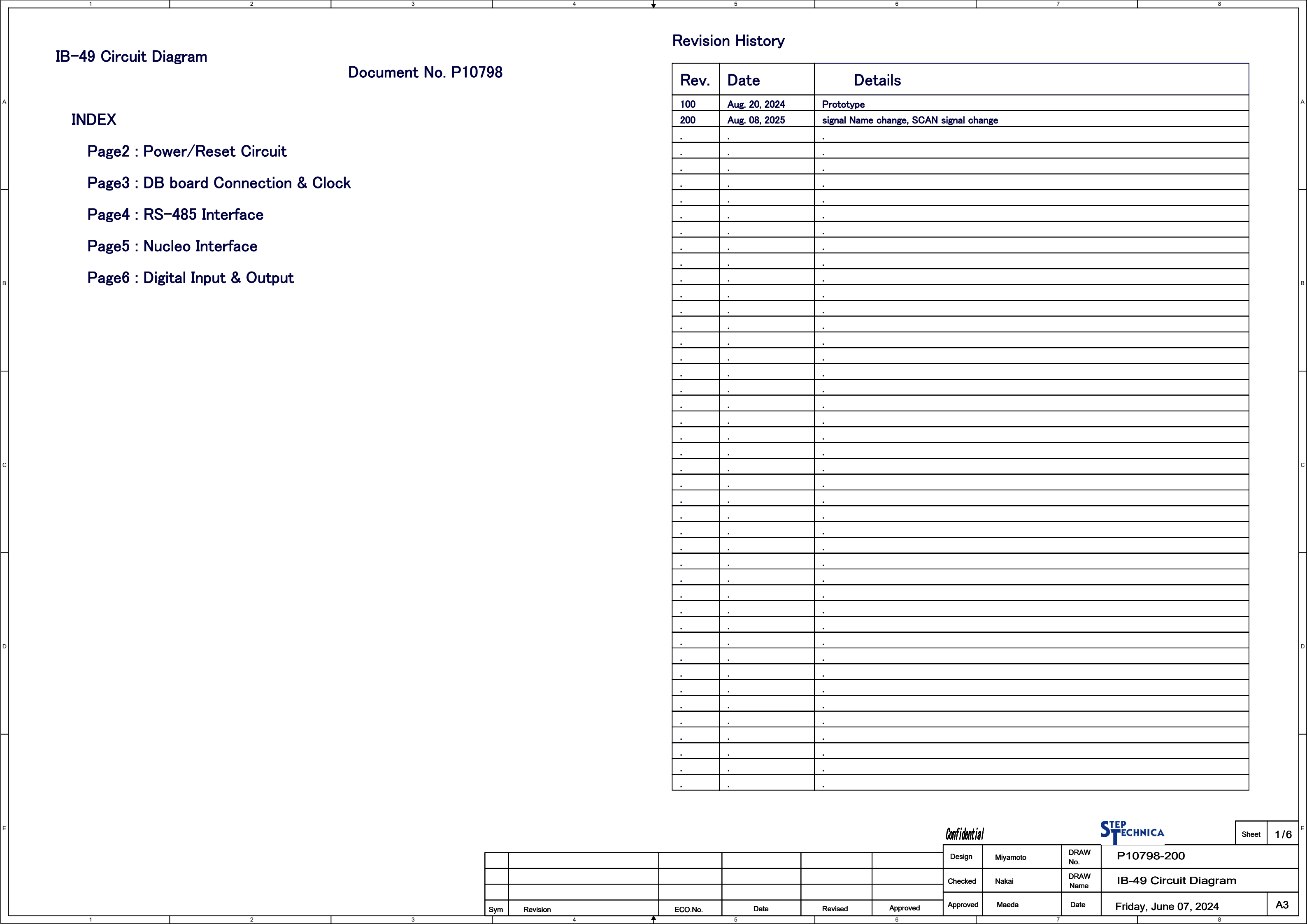
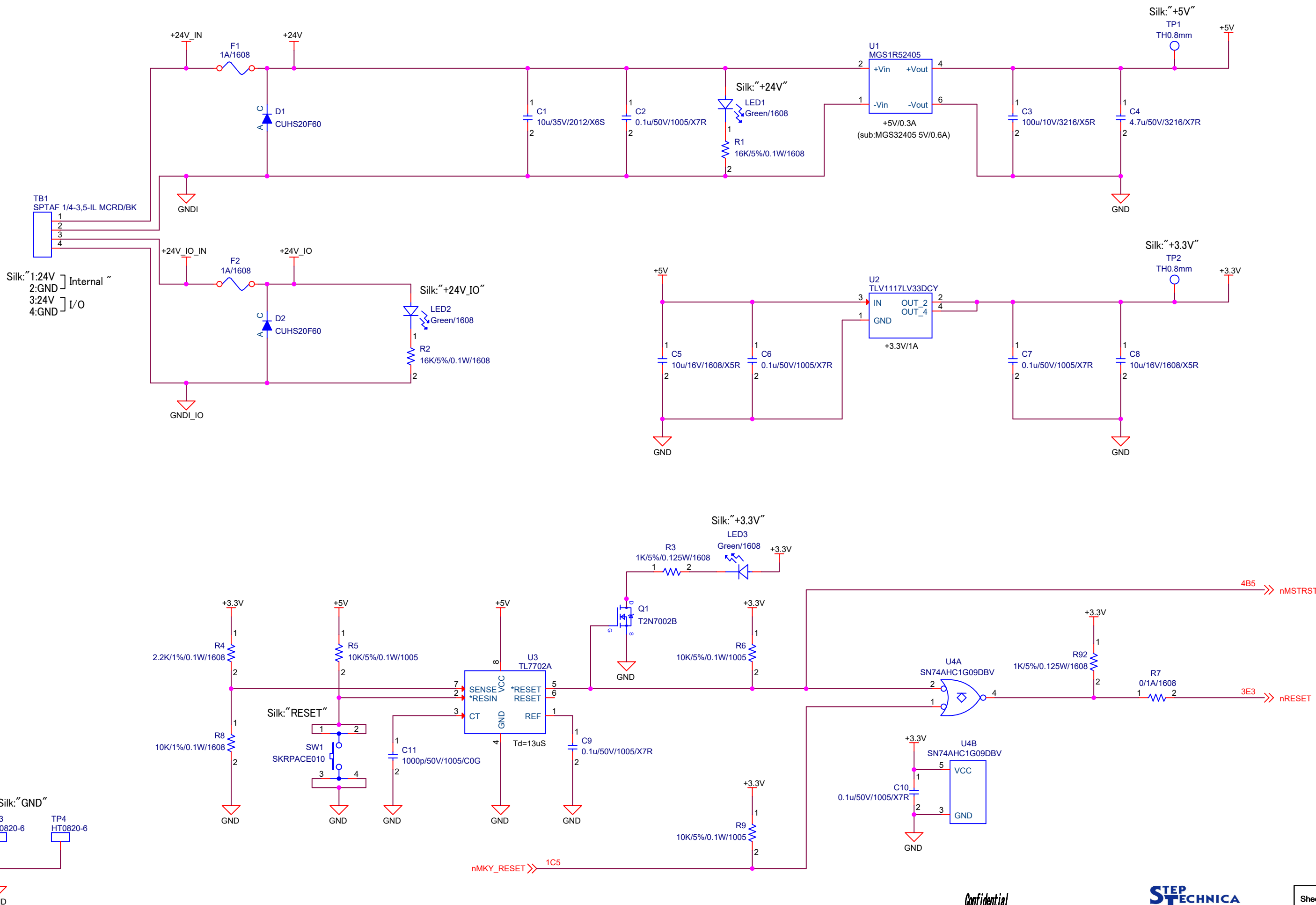


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	IB-49 Circuit Diagram			Document No. P10798		Revision History					
A	INDEX										
	Page2 : Power/Reset Circuit										
	Page3 : DB board Connection & Clock										
	Page4 : RS-485 Interface										
	Page5 : Nucleo Interface										
B	Page6 : Digital Input & Output										

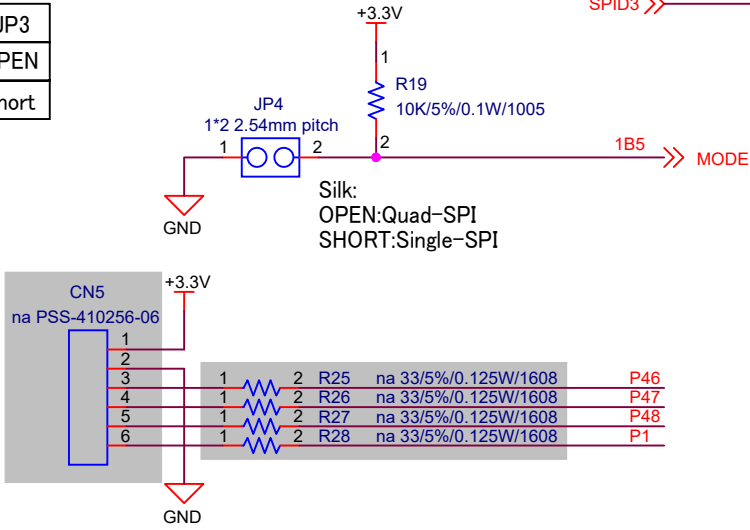
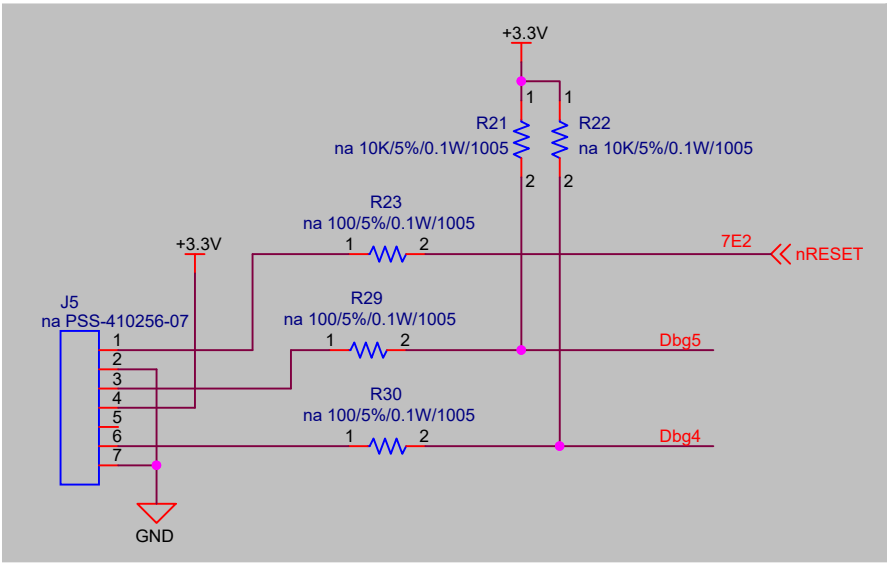
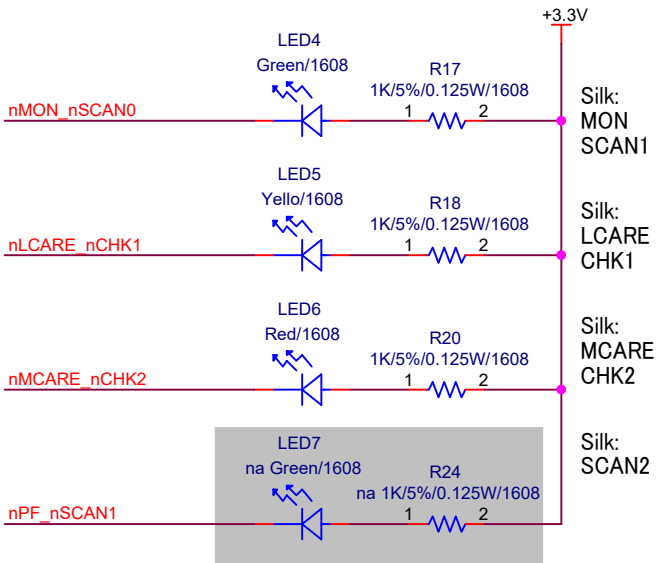
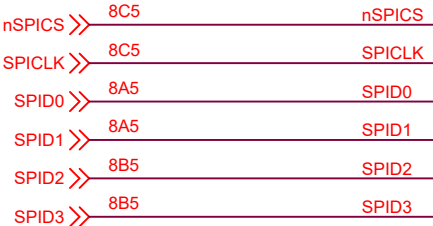
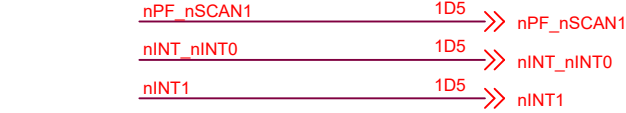
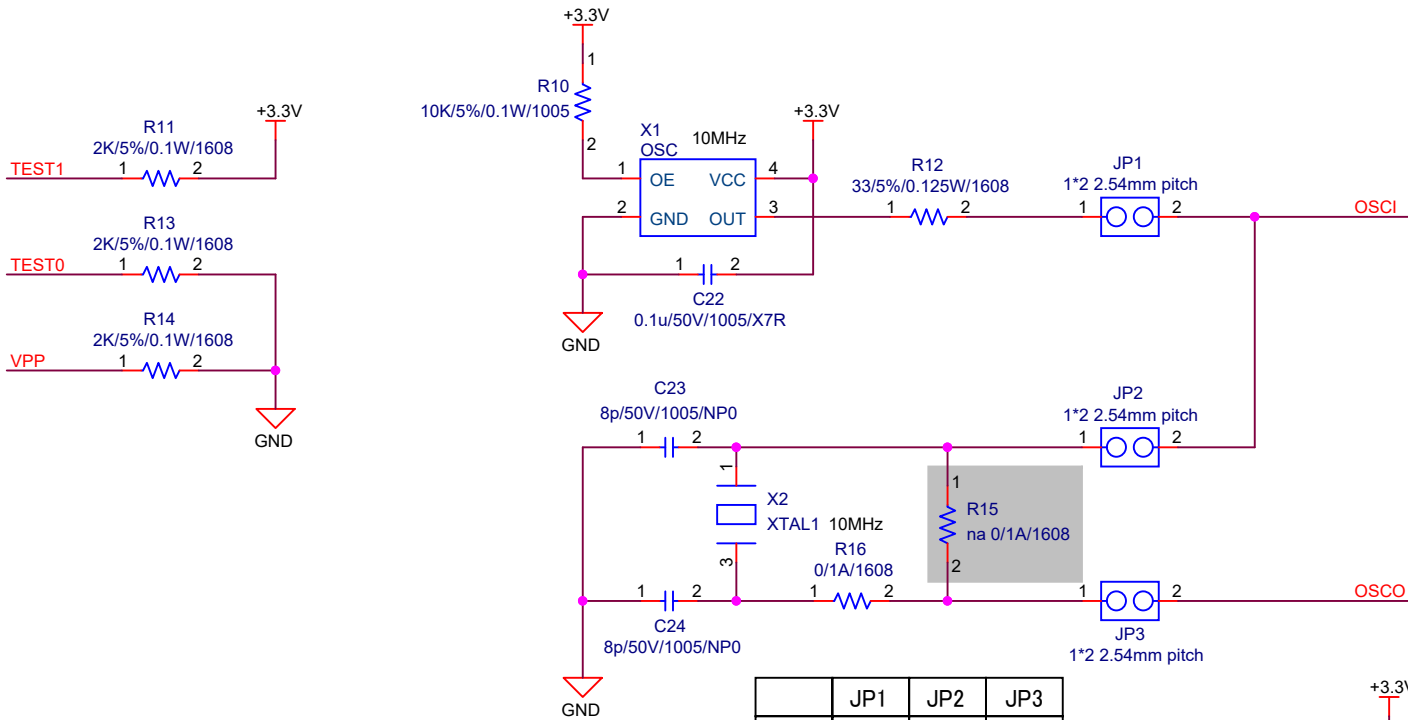
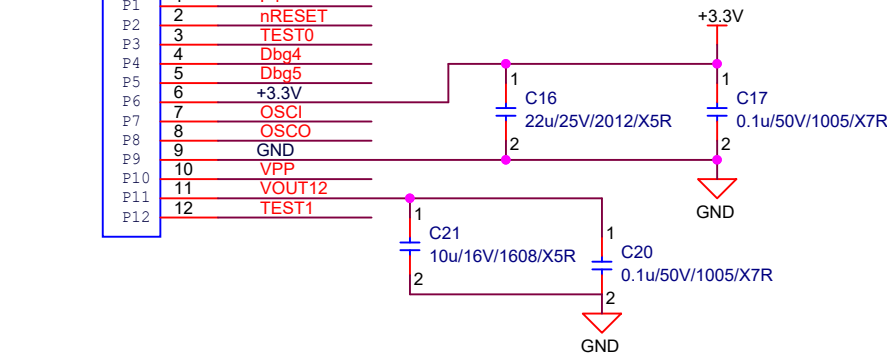
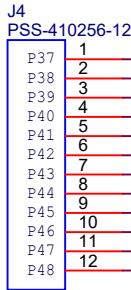
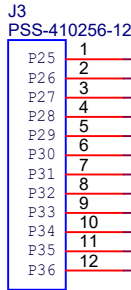
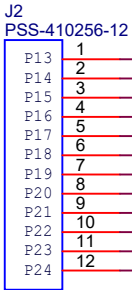
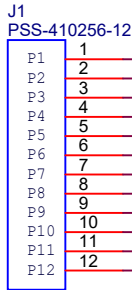
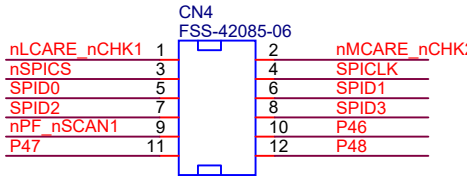
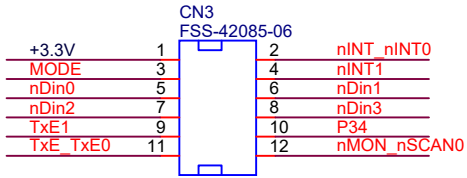
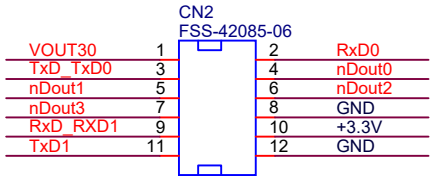
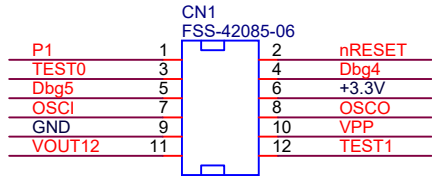
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(TP1,TP2は基板の対角に1個ずつ配置する)

Sym	Revision	ECO.No.	Date	Revised	Approved

Confidential		STEPTECHNICA		Sheet	2 / 6
Design	Miyamoto	DRAW No.	P10798-200		
Checked	Nakai	DRAW Name	IB-49 Circuit Diagram		
Approved	Maeda	Date	Friday, August 08, 2025		A3

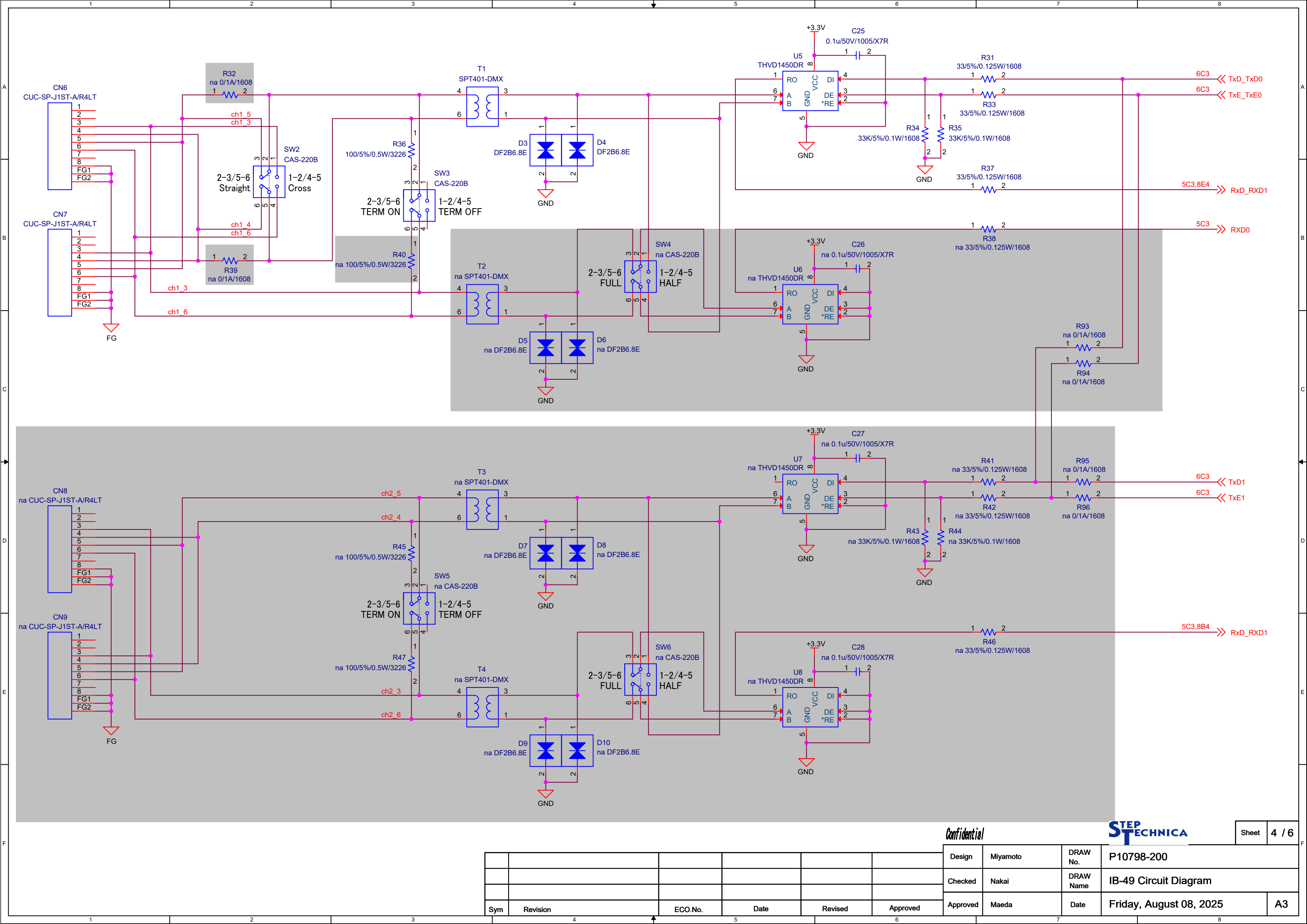


Confidential

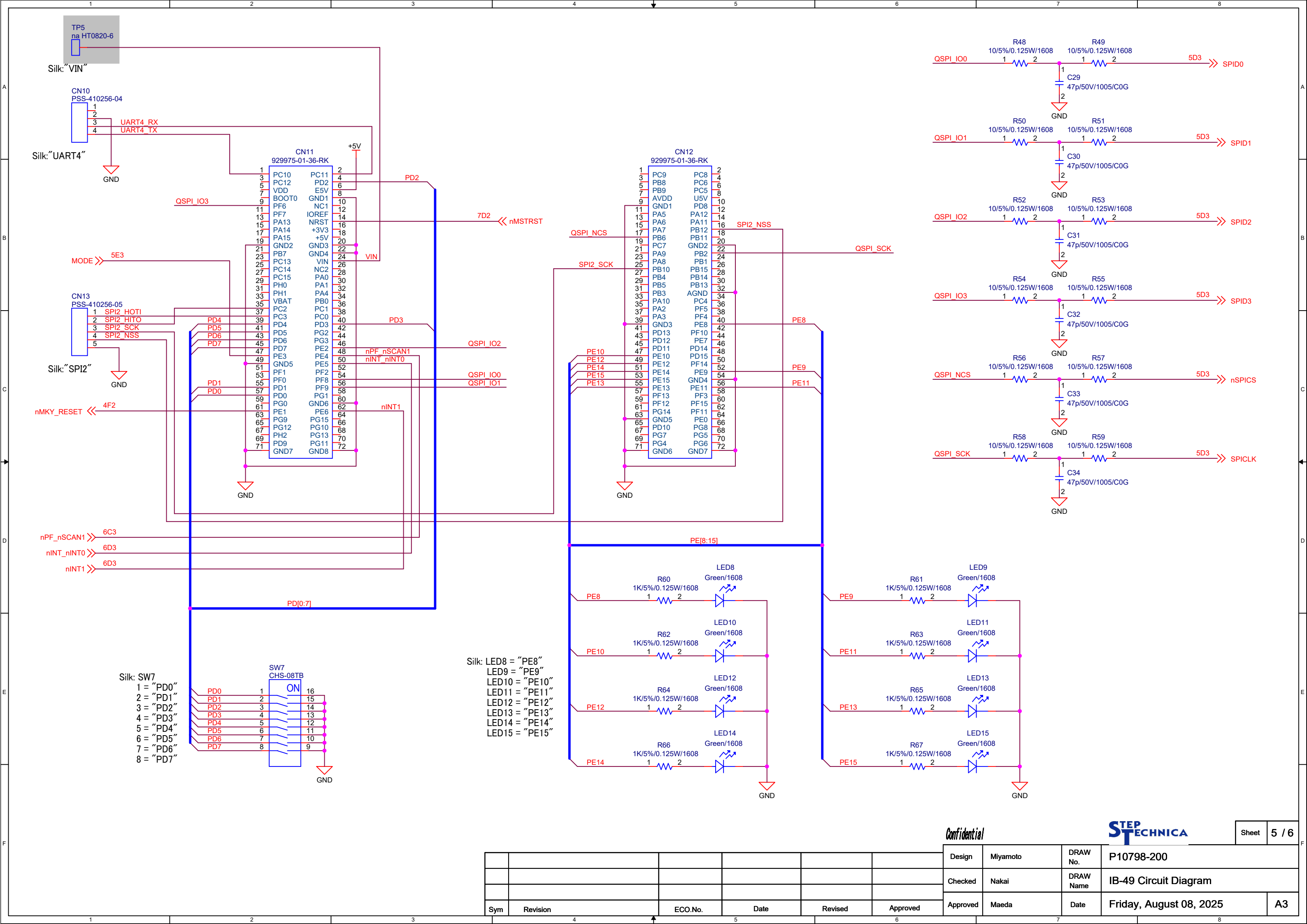
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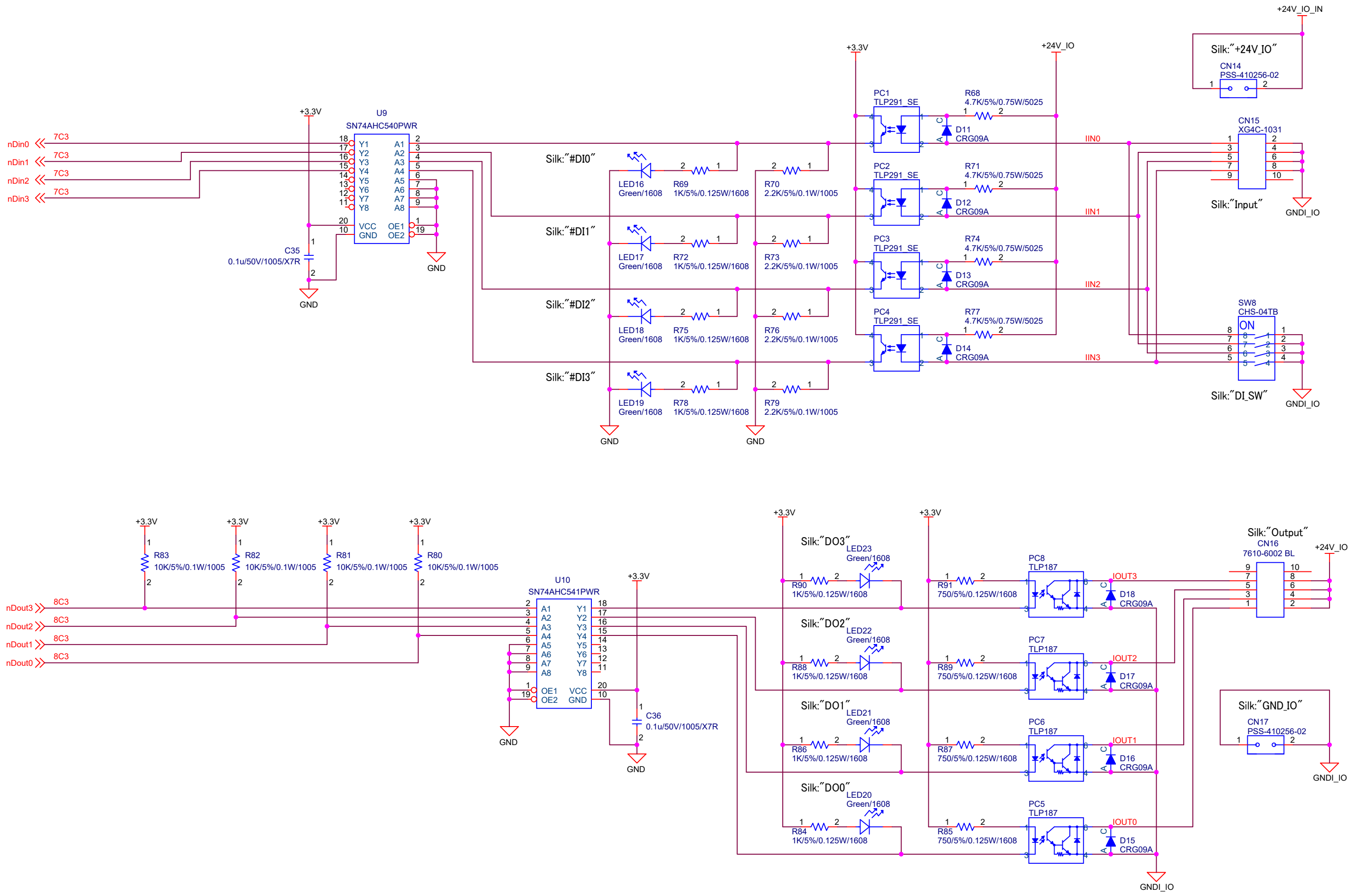
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						Design	Miyamoto	DRAW No.	P10798-200
						Checked	Nakai	DRAW Name	IB-49 Circuit Diagram
						Approved	Maeda	Date	Friday, August 08, 2025
Sym	Revision	ECO.No.	Date	Revised	Approved				A3



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Sym	Revision	ECO.No.	Date	Revised	Approved					





Confidential



						Design	Miyamoto	DRAW No.	P10798-200	
						Checked	Nakai	DRAW Name	IB-49 Circuit Diagram	
						Approved	Maeda	Date	Thursday, August 21, 2025	A3
Sym	Revision	ECO.No.	Date	Revised	Approved					