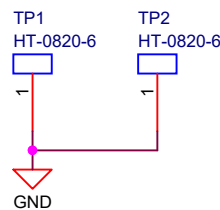
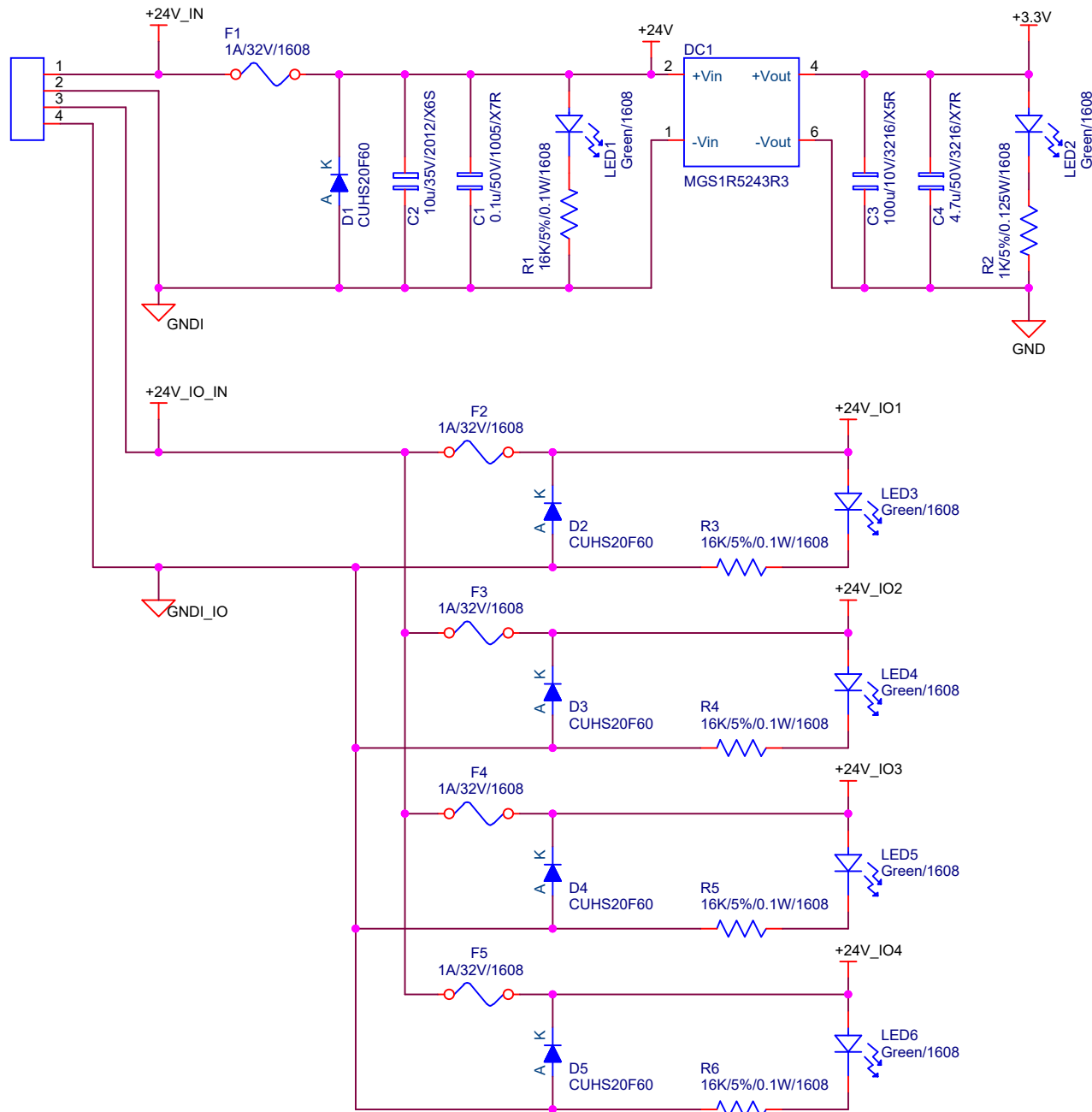
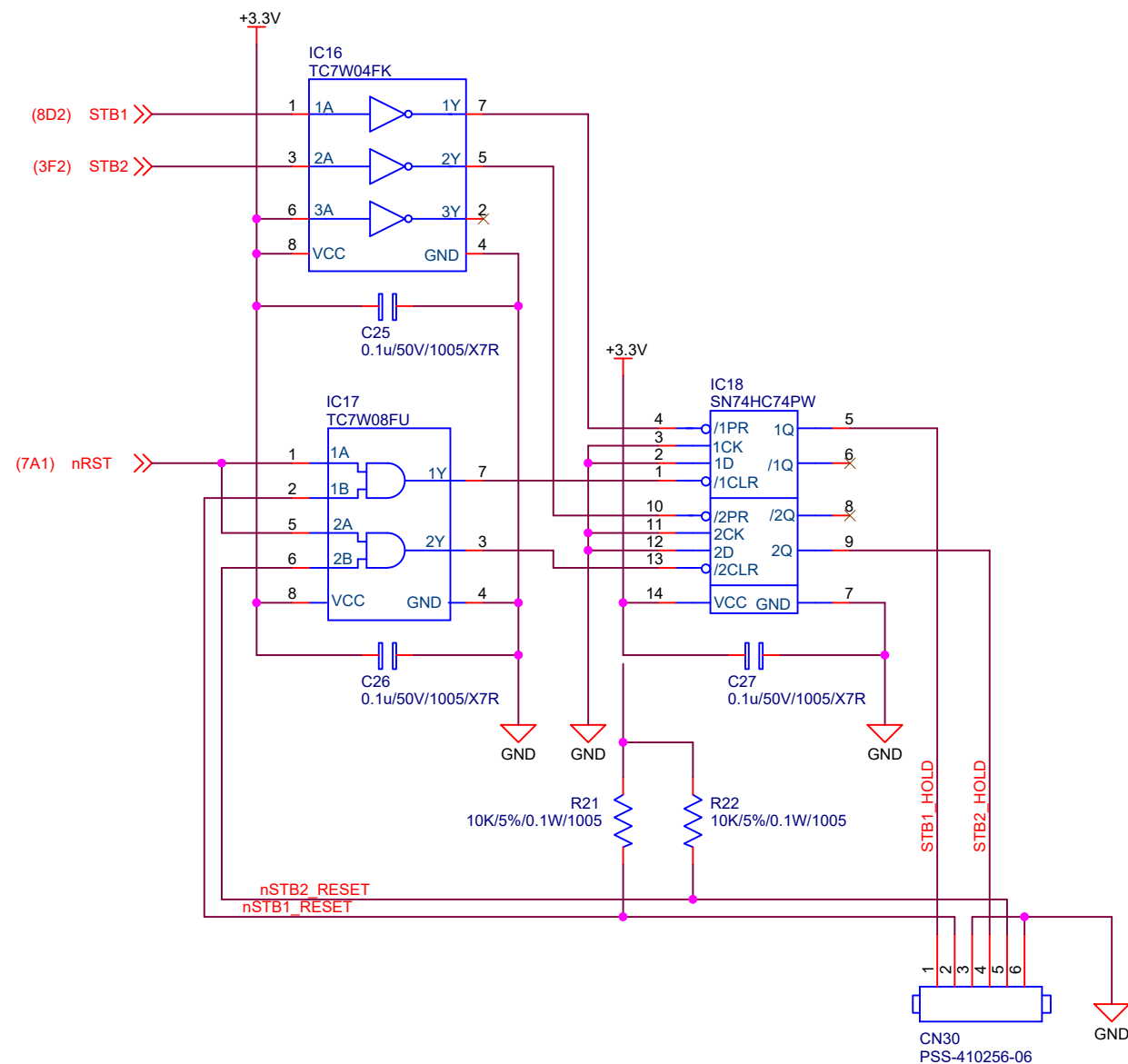
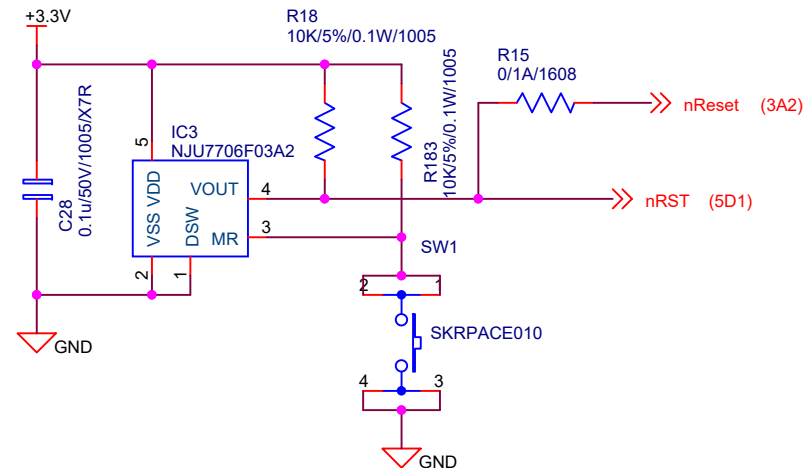


CN10
SPTAF 1/4-3,5-IL MCRD/BK



(TP1,TP2は基板の対角に1個ずつ配置する)

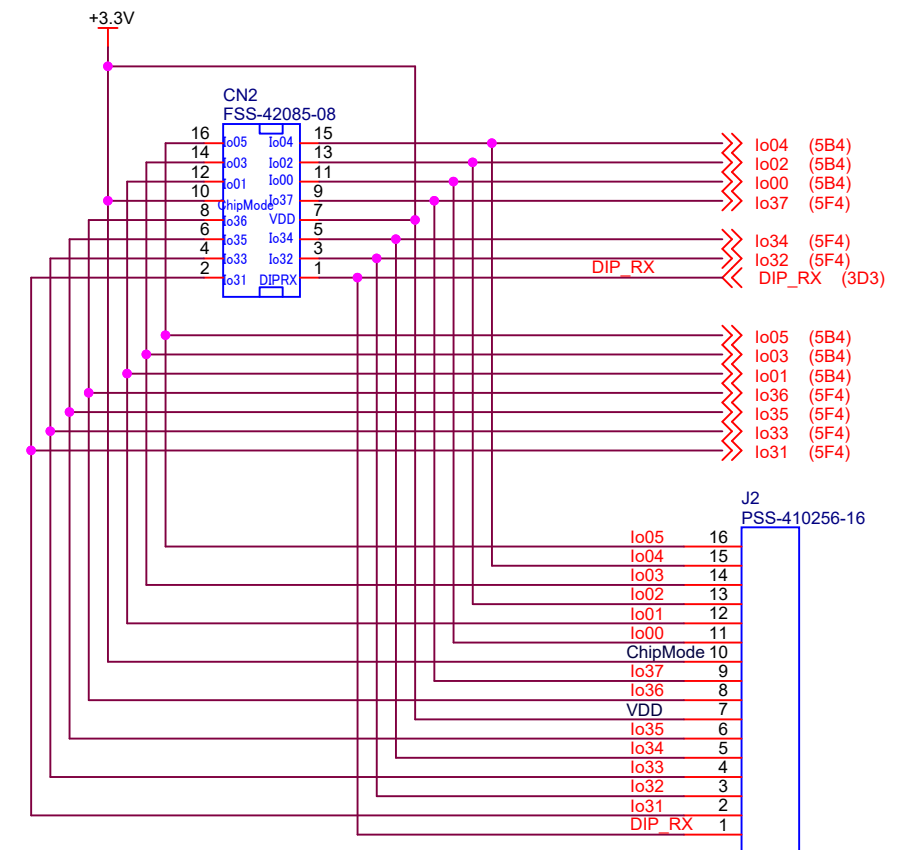
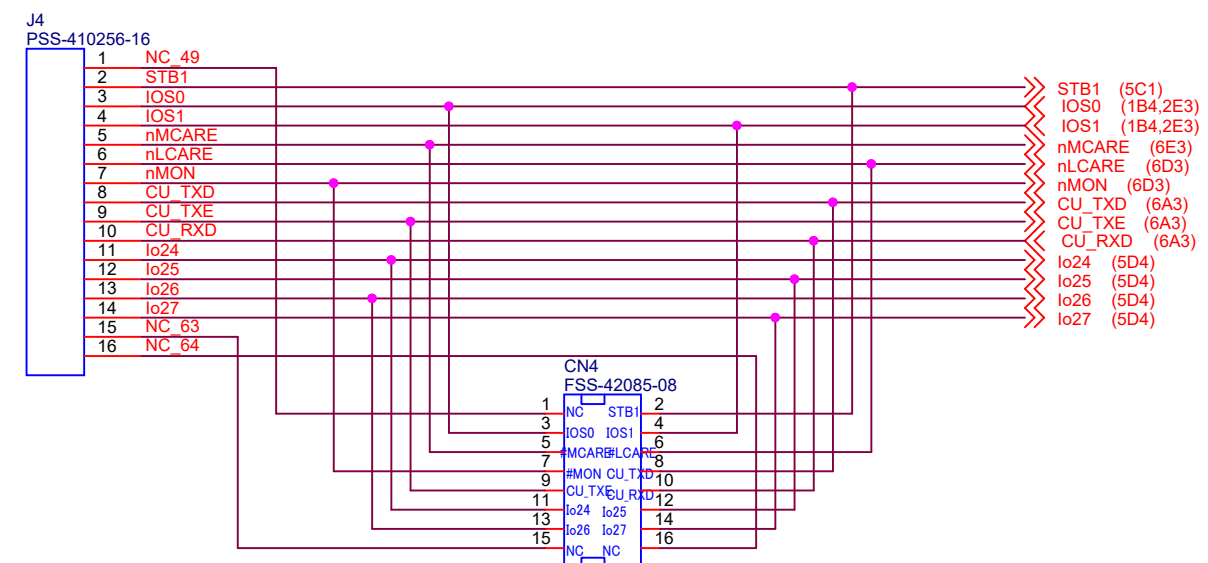
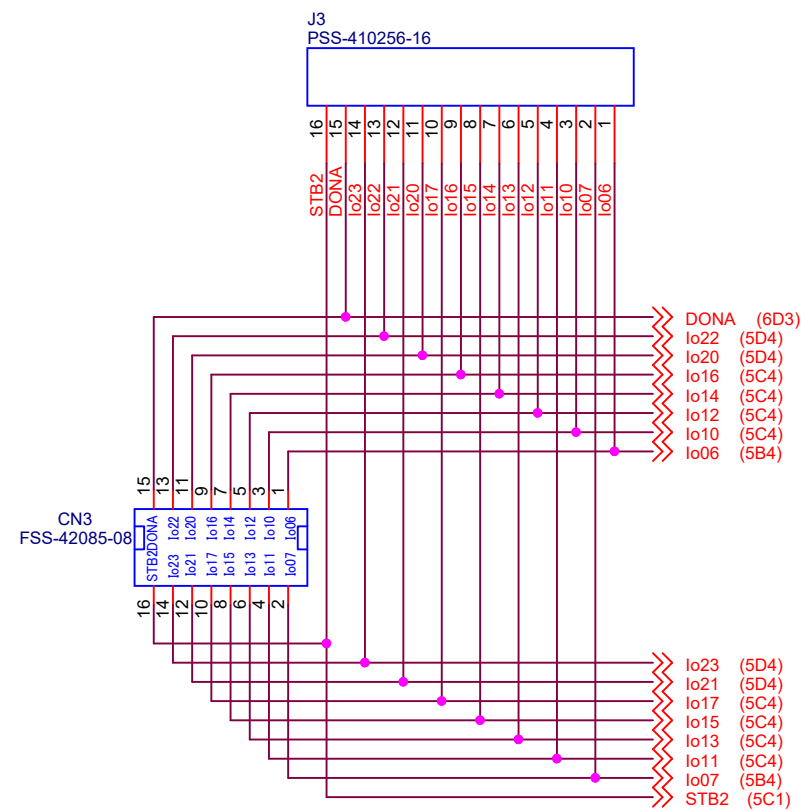
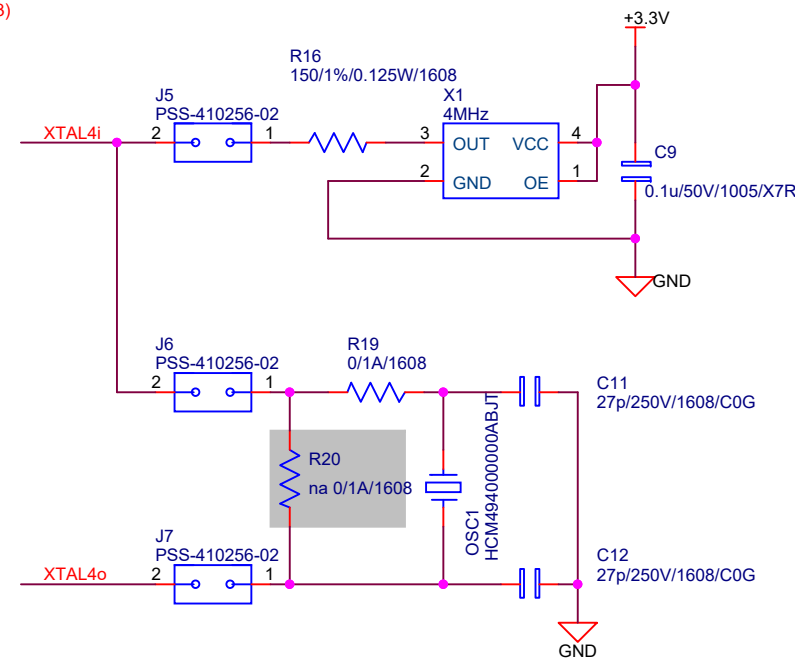
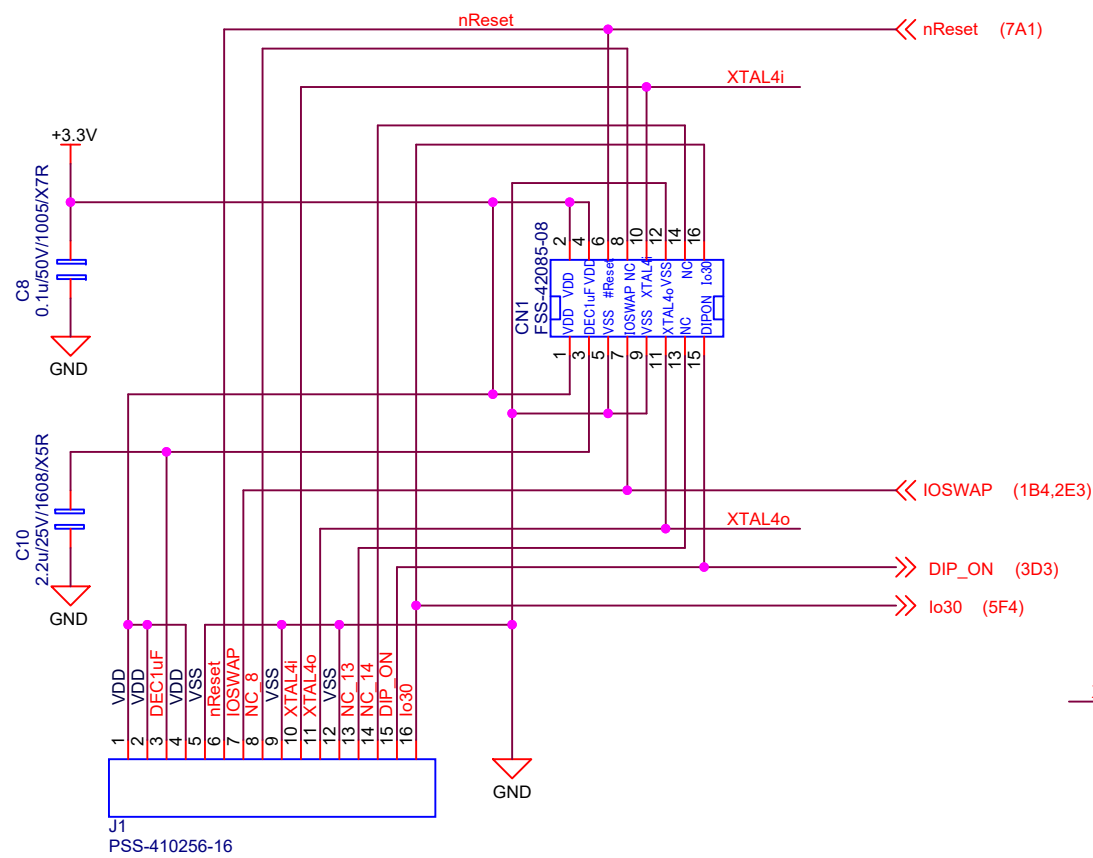


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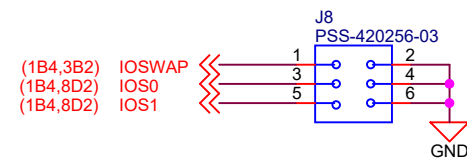
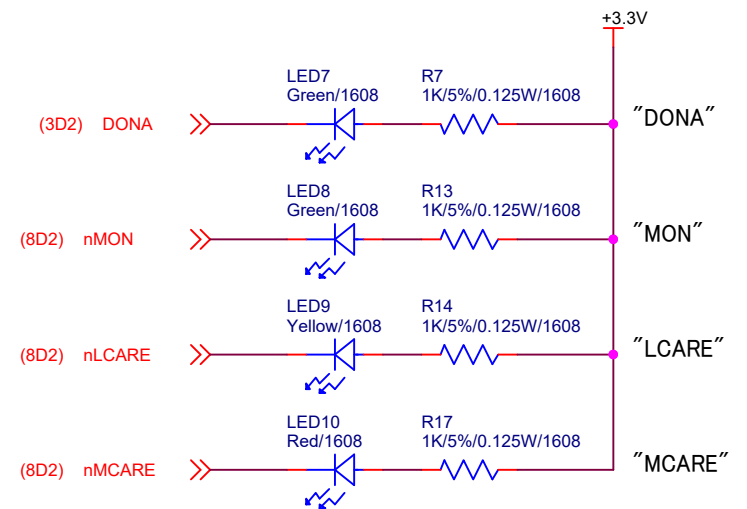
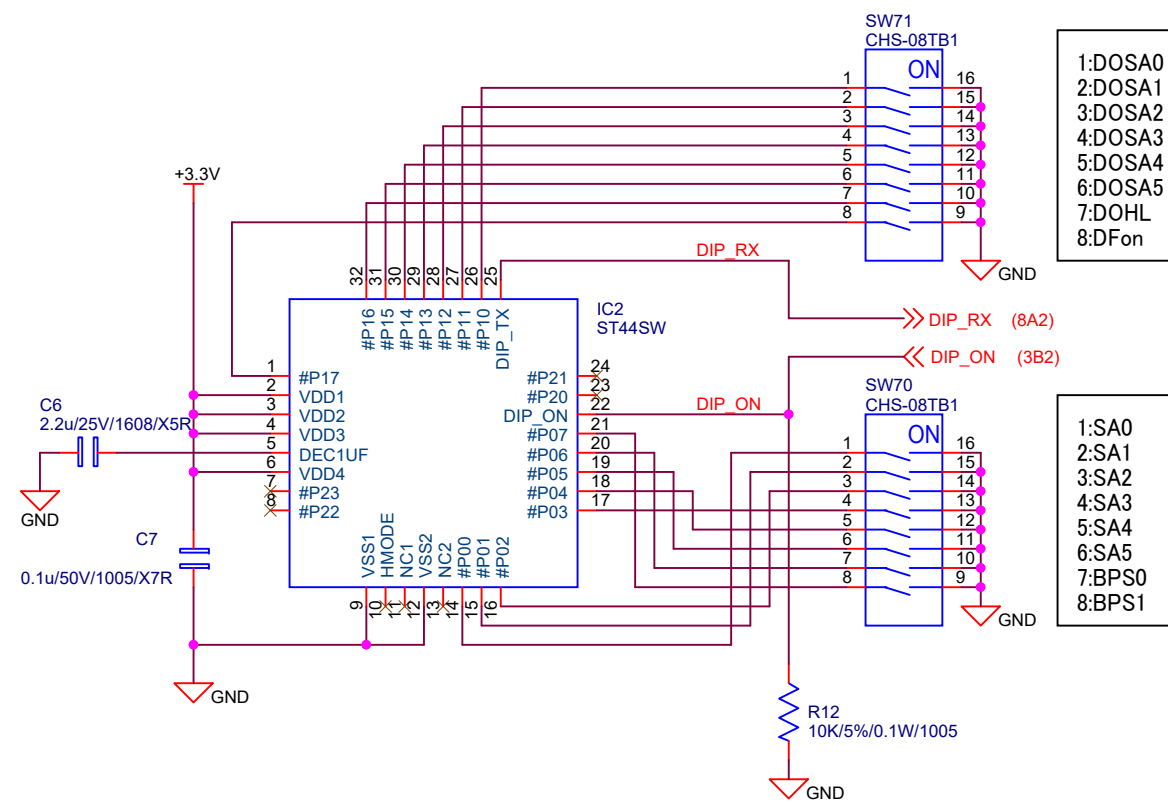
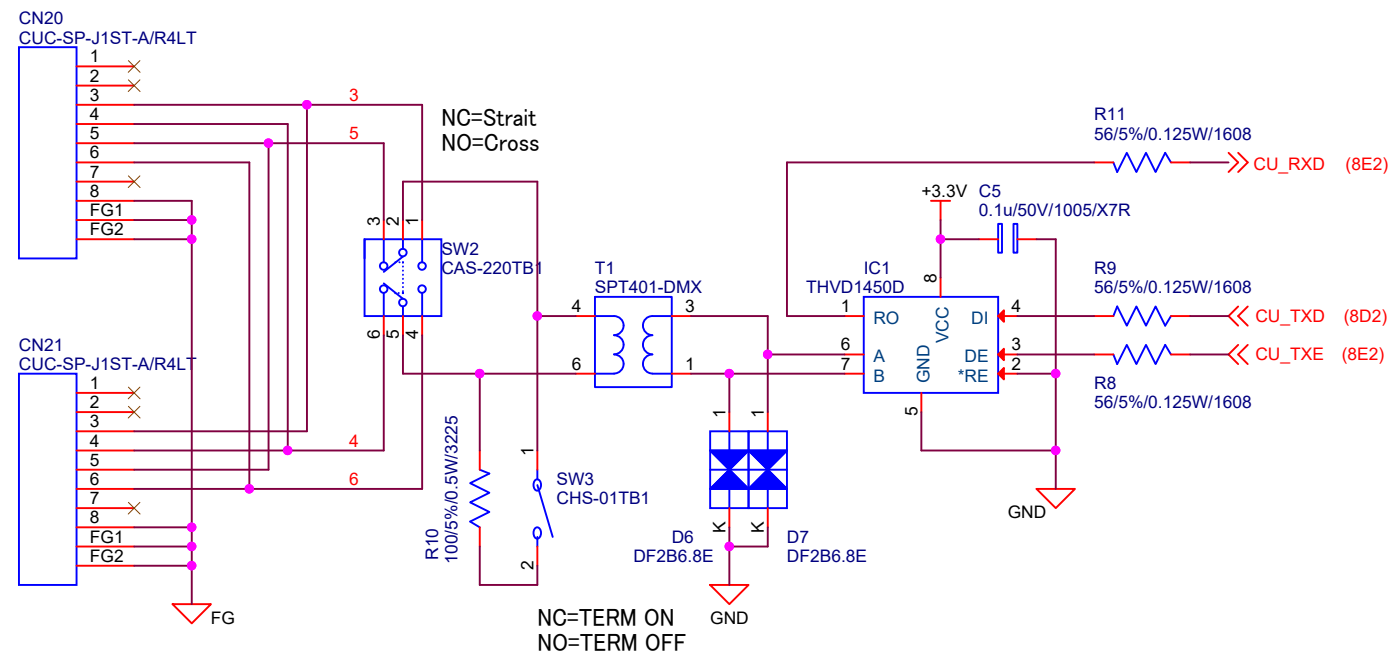
STEP
TECHNICA

Sheet 1 / 8

						Design	M.Adachi	DRAW No.	P10585-200	
						Checked	H.Miyamoto	DRAW Name	IB-44IO32A Circuit Diagram	
						Approved	T.Maeda	Date	Friday, January 05, 2024	
Sym	Revision	ECO.No.	Date	Revised	Approved				A3	



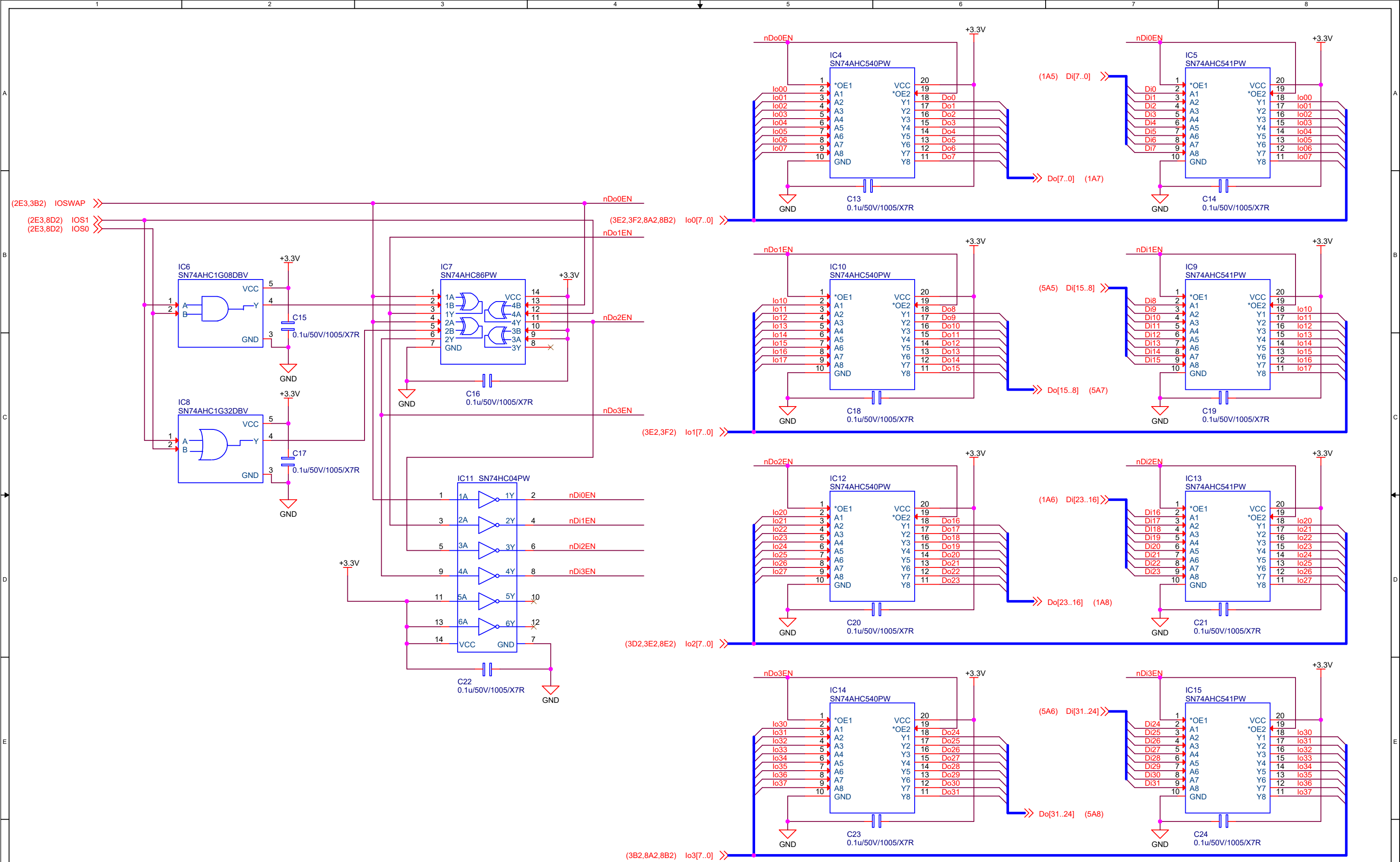
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						Checked	H.Miyamoto	DRAW Name	IB-44IO32A Circuit Diagram	
						Approved	T.Maeda	Date	Friday, January 05, 2024	A3
Sym	Revision	ECO.No.	Date	Revised	Approved					



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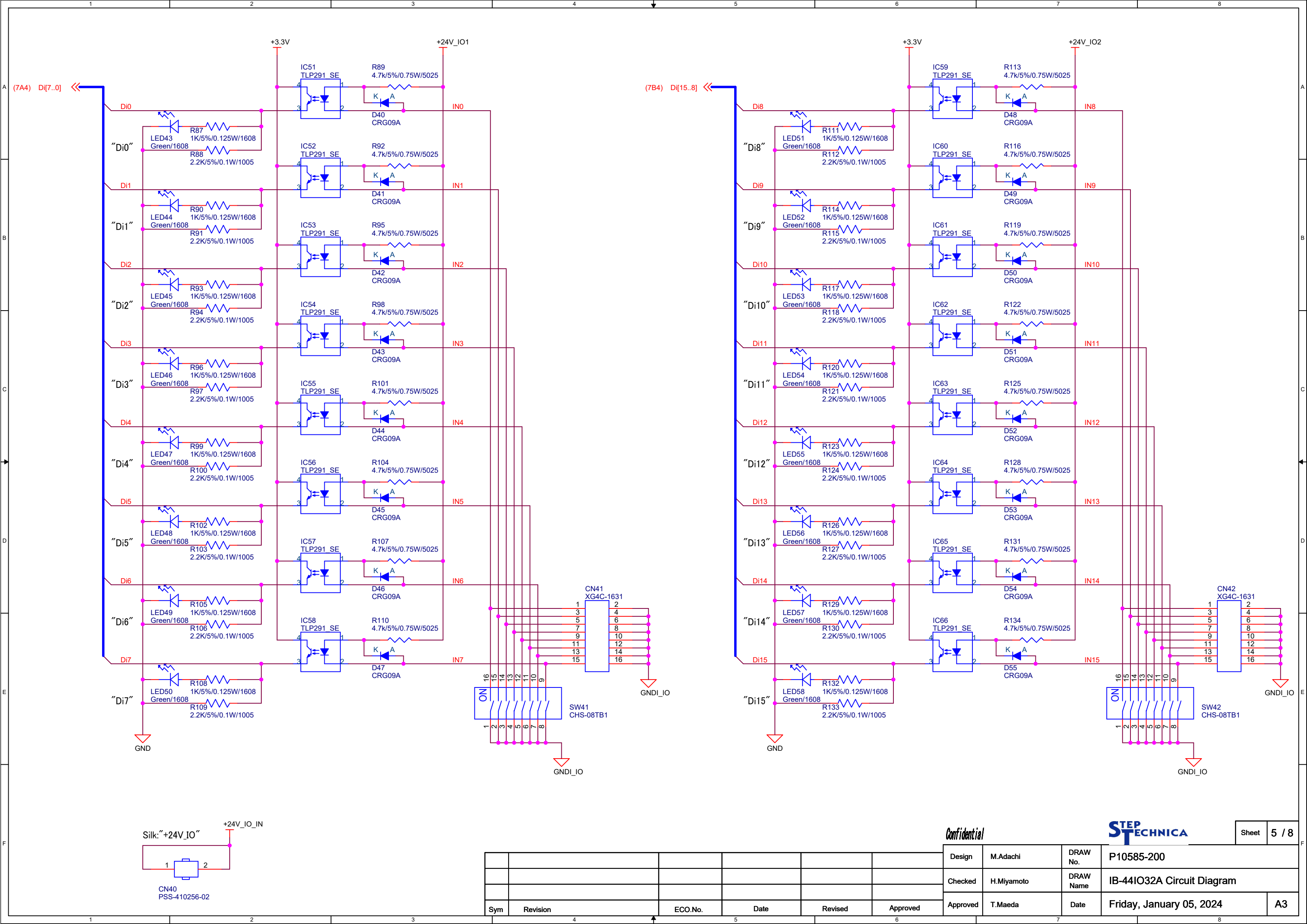
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						Checked	H.Miyamoto	DRAW Name	IB-44IO32A Circuit Diagram
						Approved	T.Maeda	Date	Friday, January 05, 2024
Sym	Revision	ECO.No.	Date	Revised	Approved				A3



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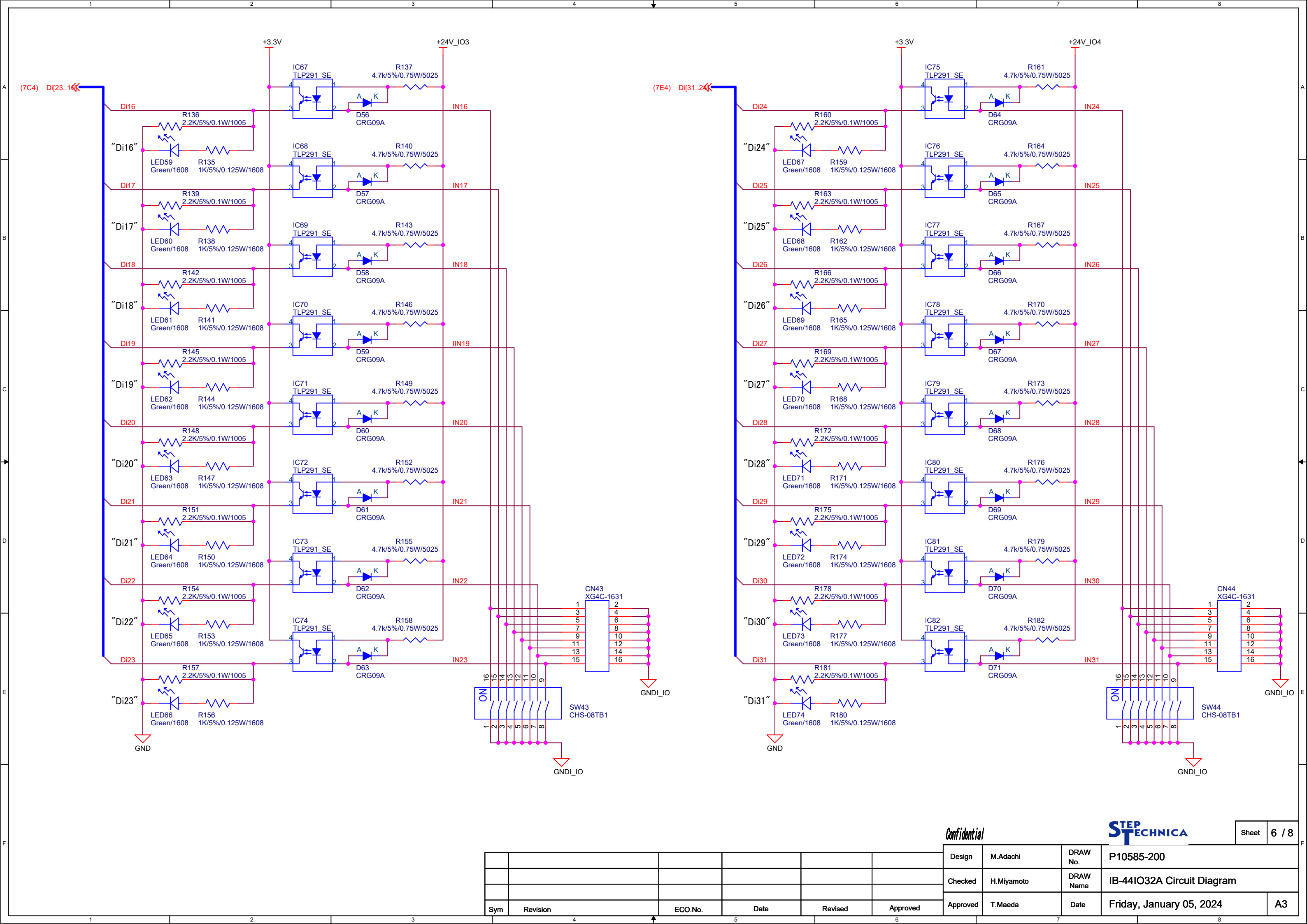


						Design	M.Adachi	DRAW No.	P10585-200
						Checked	H.Miyamoto	DRAW Name	IB-44IO32A Circuit Diagram
						Approved	T.Maeda	Date	Friday, January 05, 2024
Sym	Revision	ECO.No.	Date	Revised	Approved				A3



Sym	Revision	ECO.No.	Date	Revised	Approved

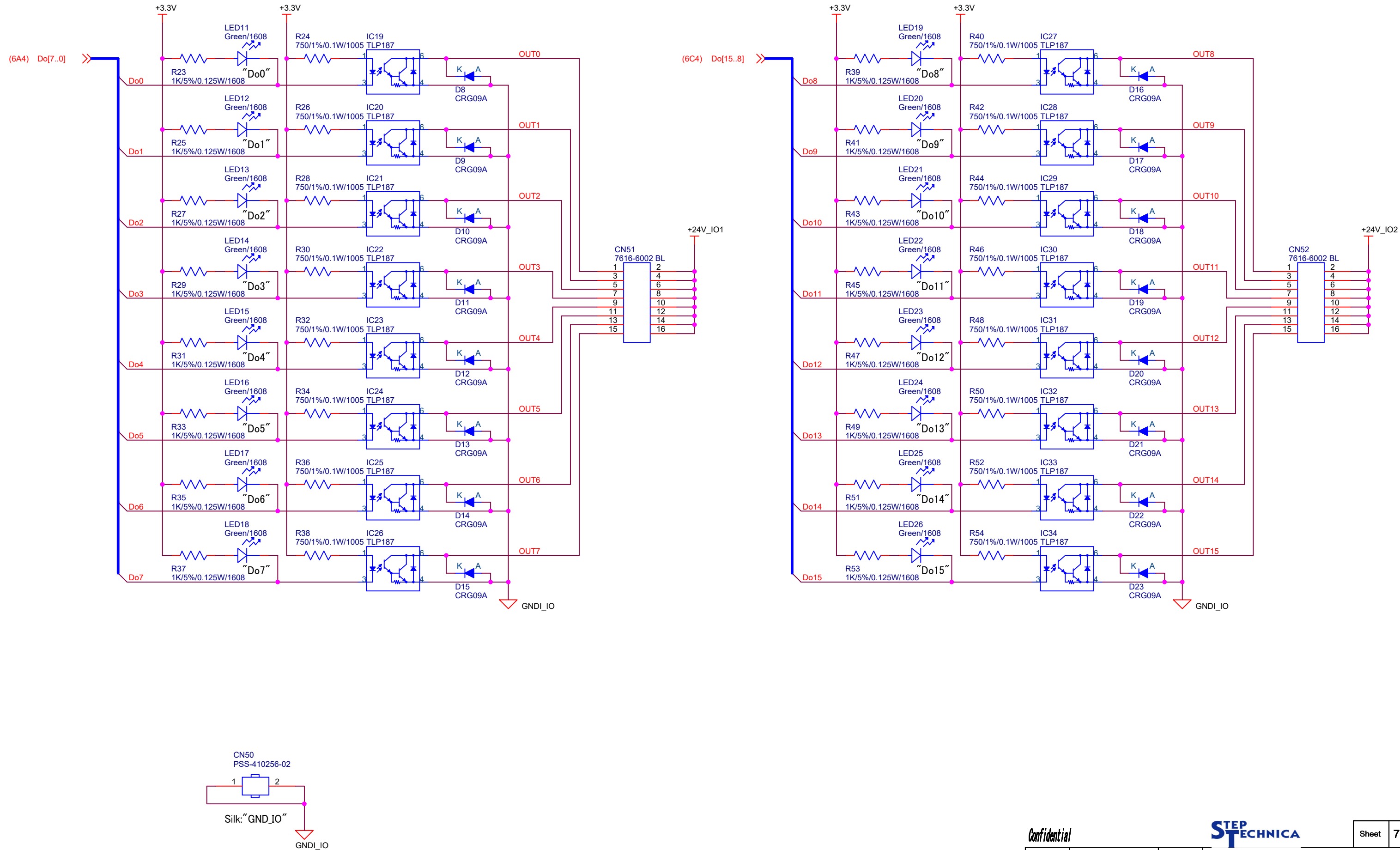
Design	M.Adachi	DRAW No.	P10585-200
Checked	H.Miyamoto	DRAW Name	IB-44IO32A Circuit Diagram
Approved	T.Maeda	Date	Friday, January 05, 2024
			A3



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Sym	Revision	ECO.No.	Date	Revised	Approved				A3

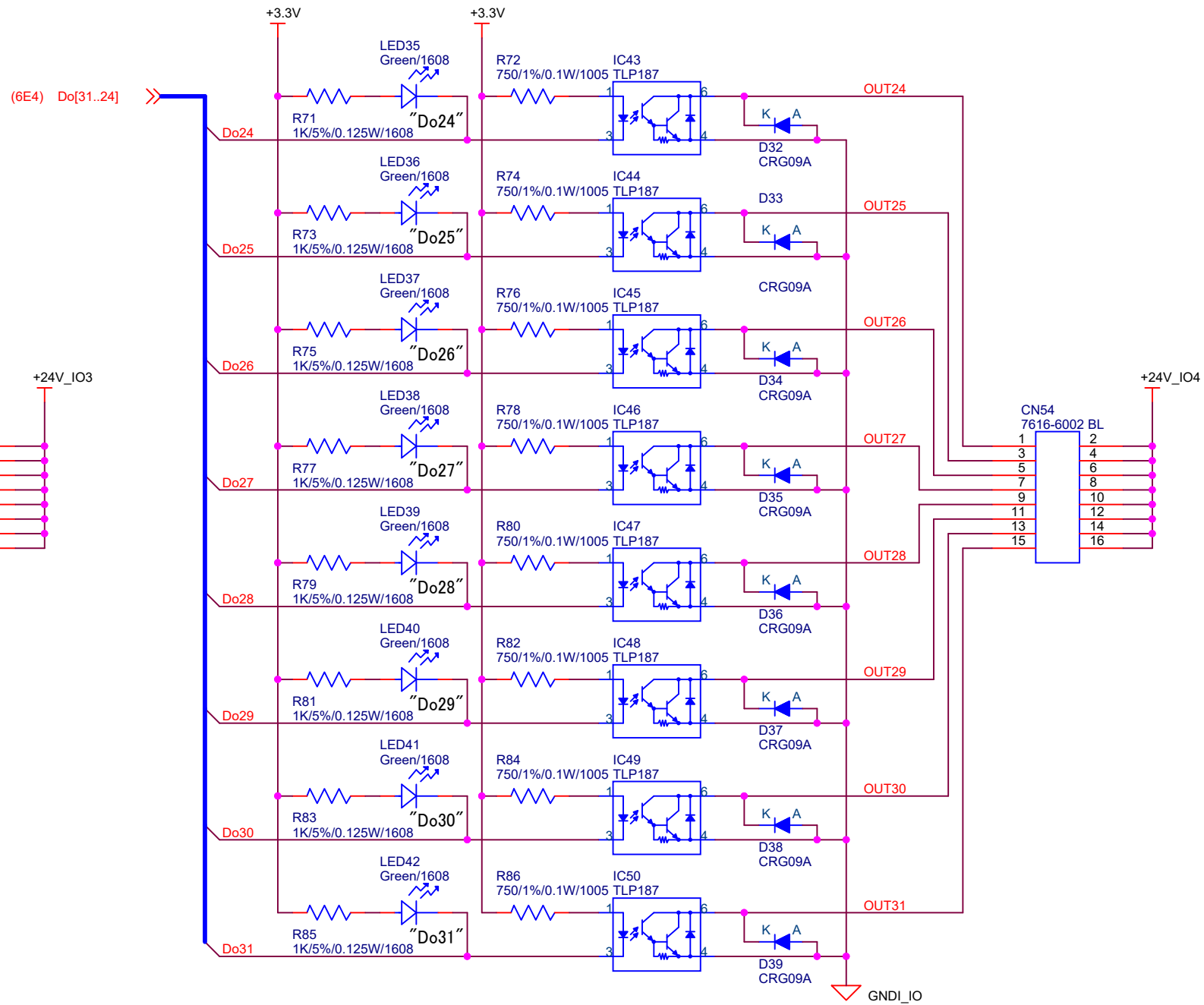
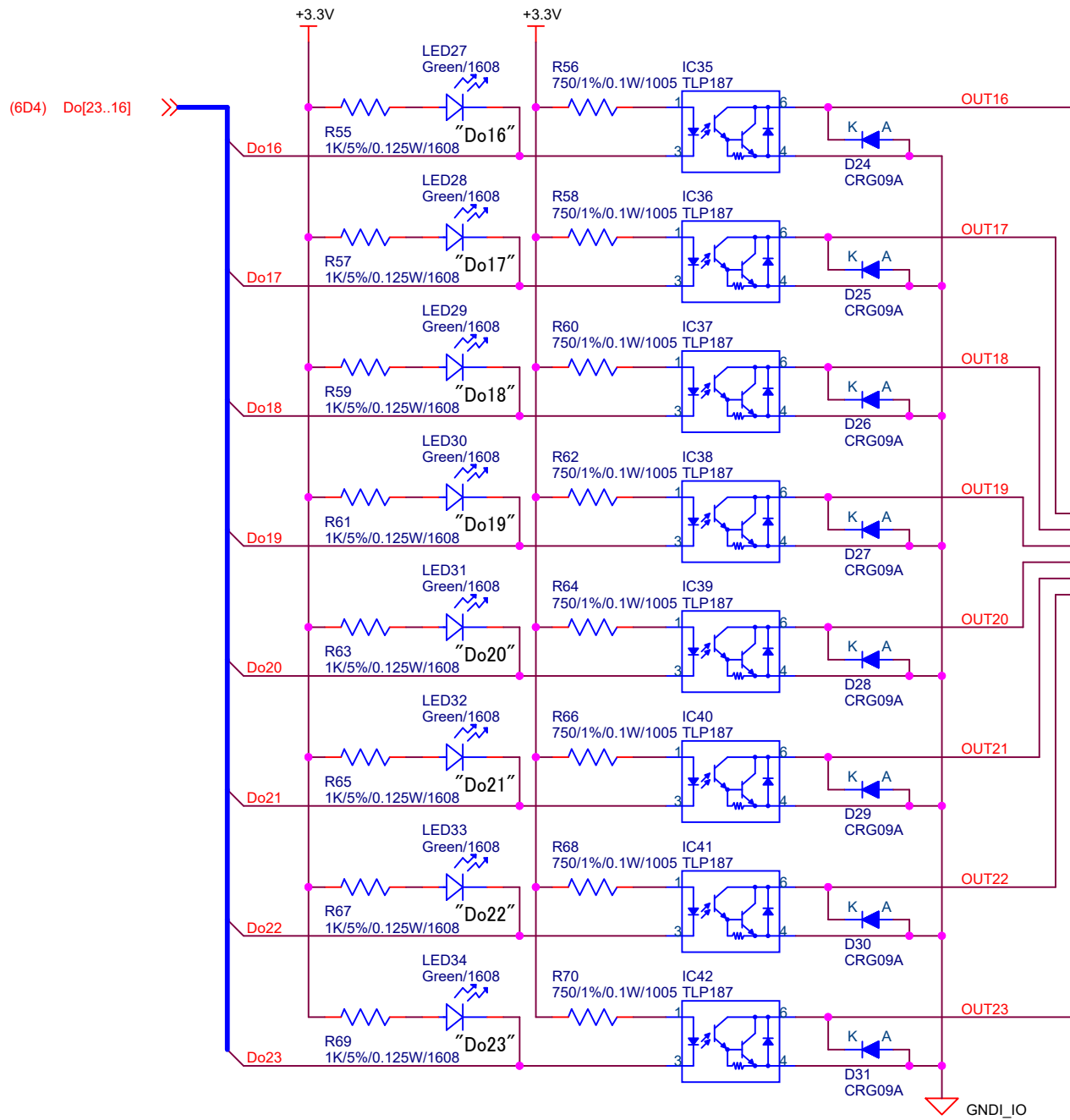


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STEP
TECHNICA

Sheet 7 / 8

						Design	M.Adachi	DRAW No.	P10585-200
						Checked	H.Miyamoto	DRAW Name	IB-44IO32A Circuit Diagram
						Approved	T.Maeda	Date	Friday, January 05, 2024
Sym	Revision	ECO.No.	Date	Revised	Approved				A3



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STEPTECHNICA

Sheet 8 / 8

						Design	M.Adachi	DRAW No.	P10585-200
						Checked	H.Miyamoto	DRAW Name	IB-44IO32A Circuit Diagram
						Approved	T.Maeda	Date	Friday, January 05, 2024
Sym	Revision	ECO.No.	Date	Revised	Approved				A3