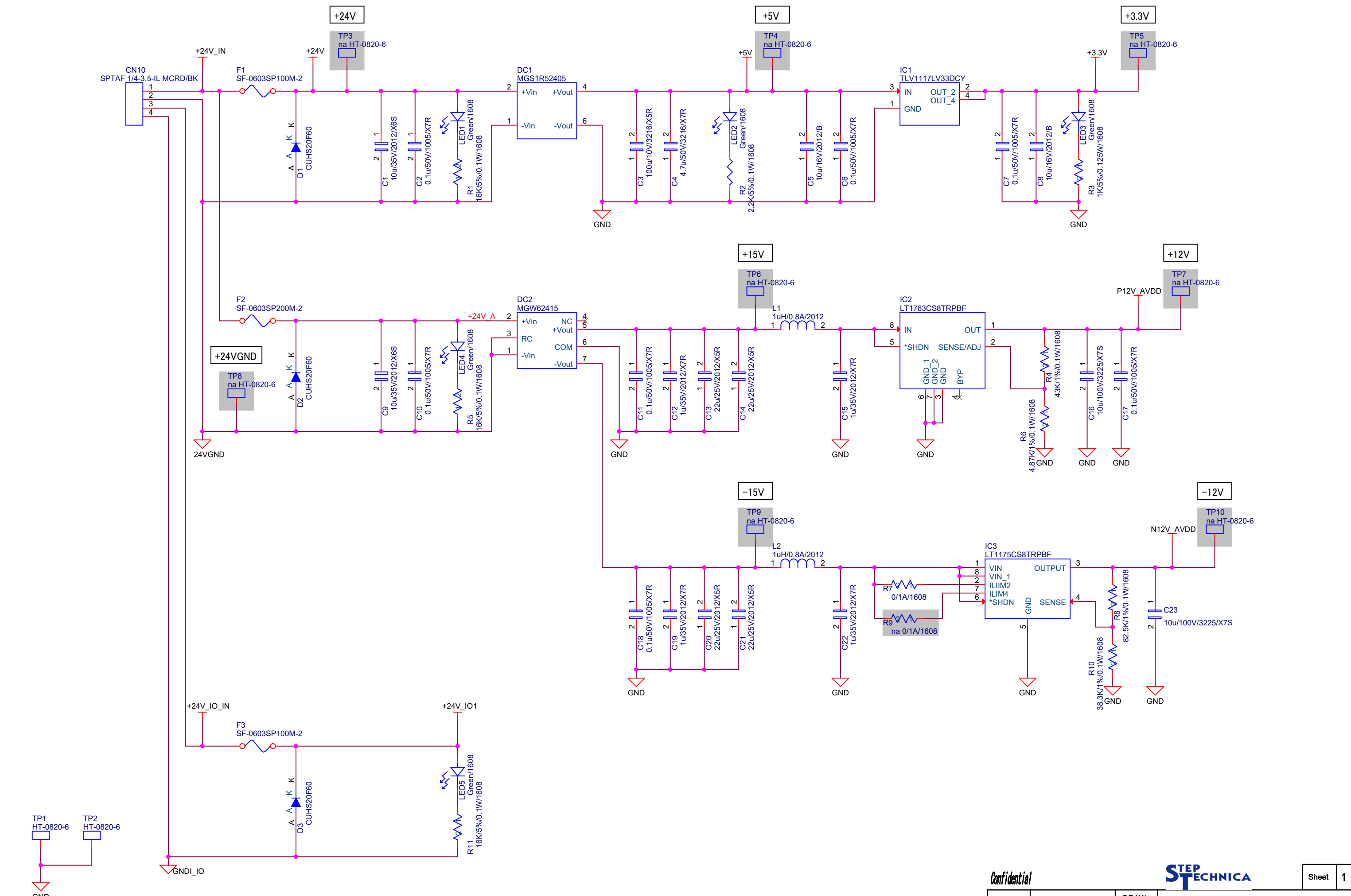
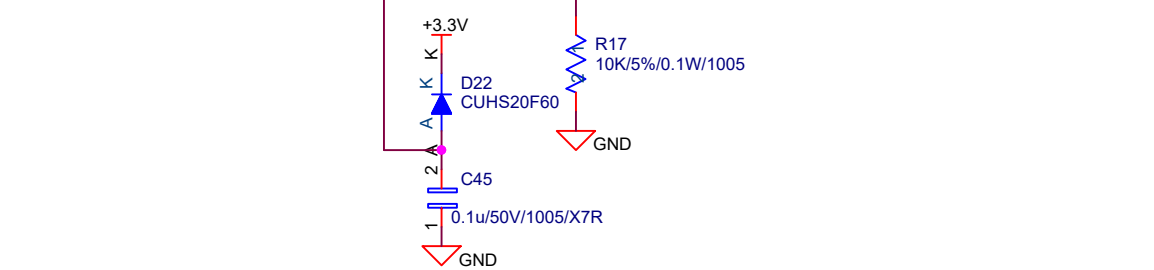
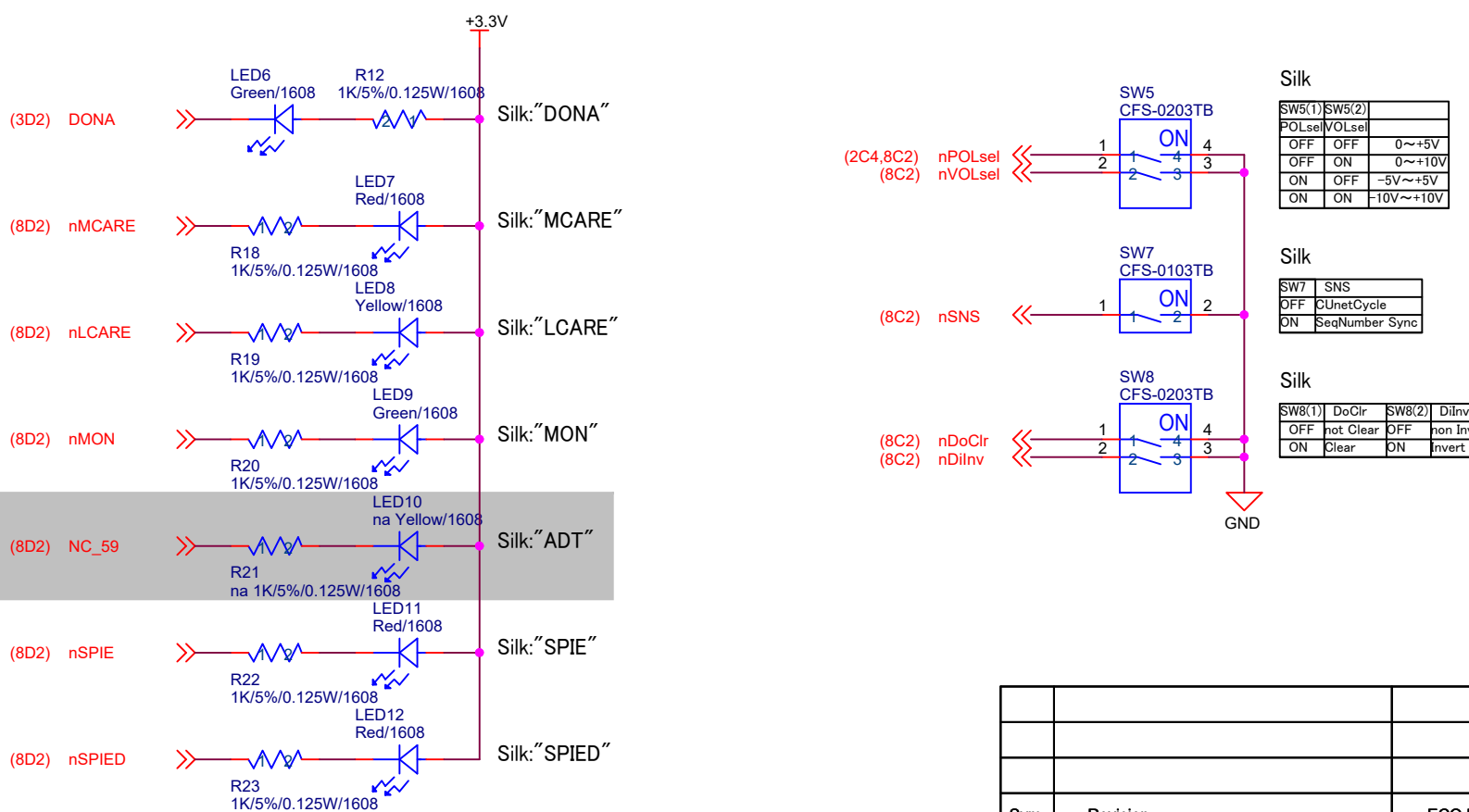
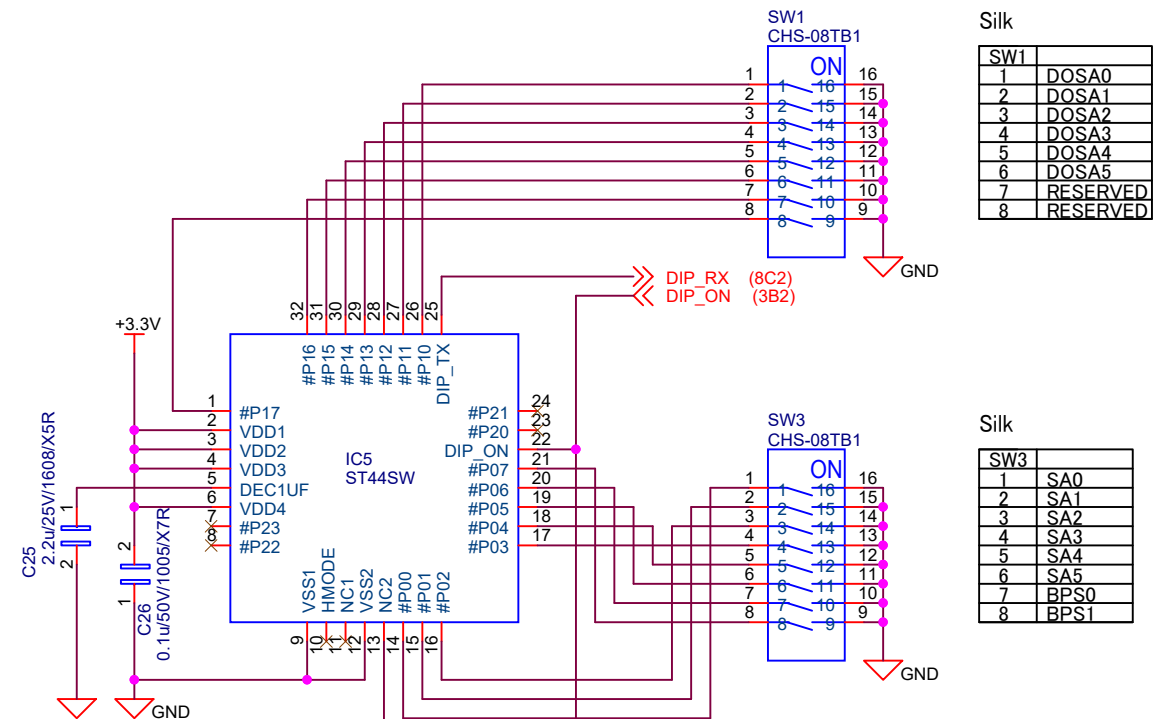
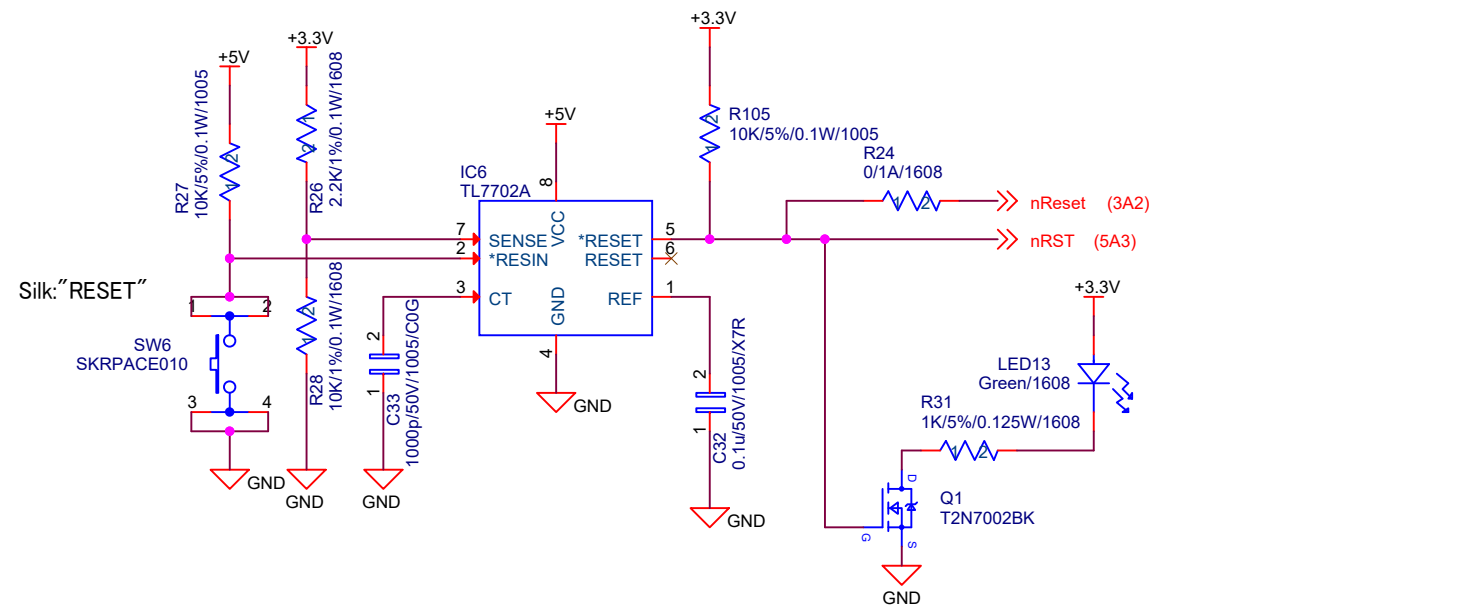
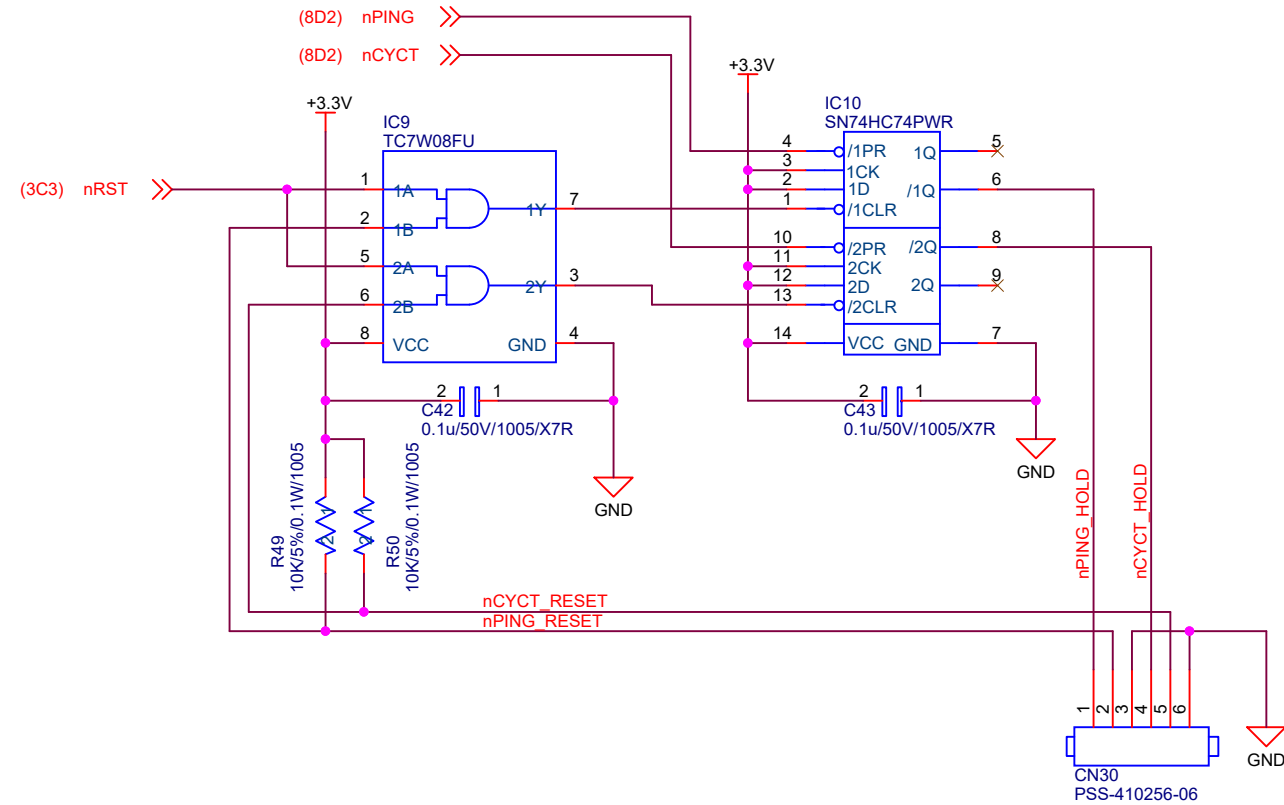
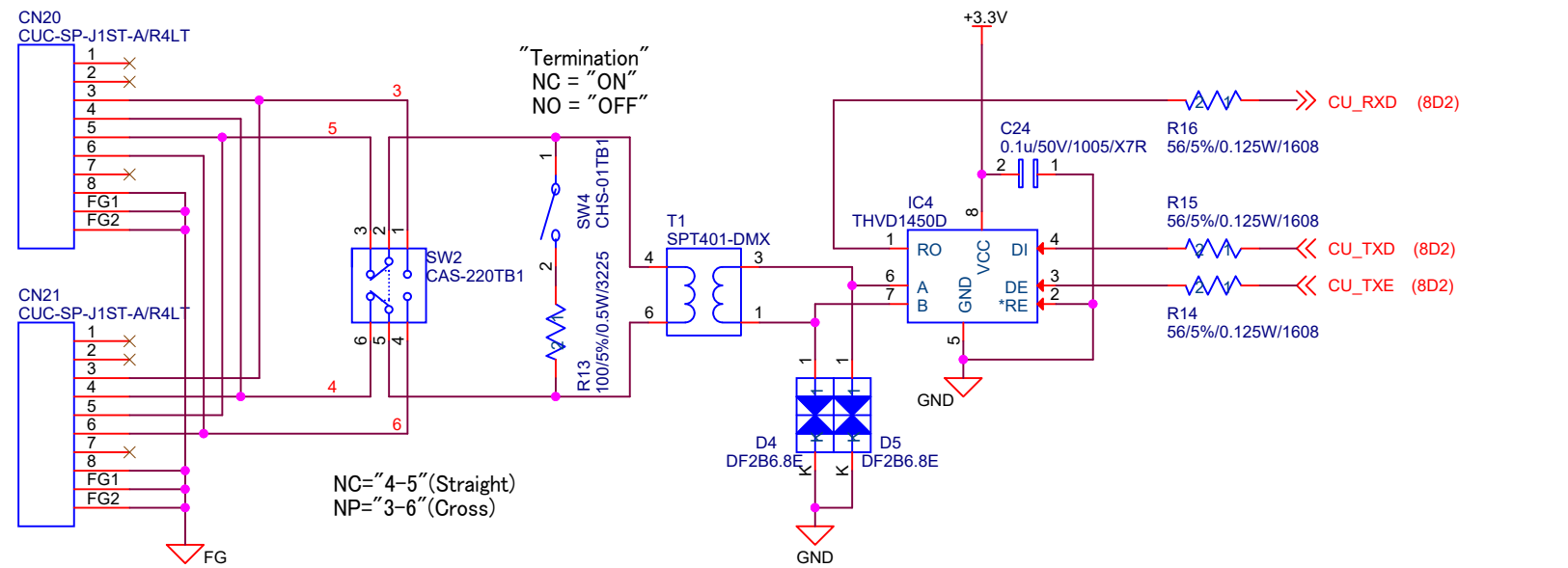


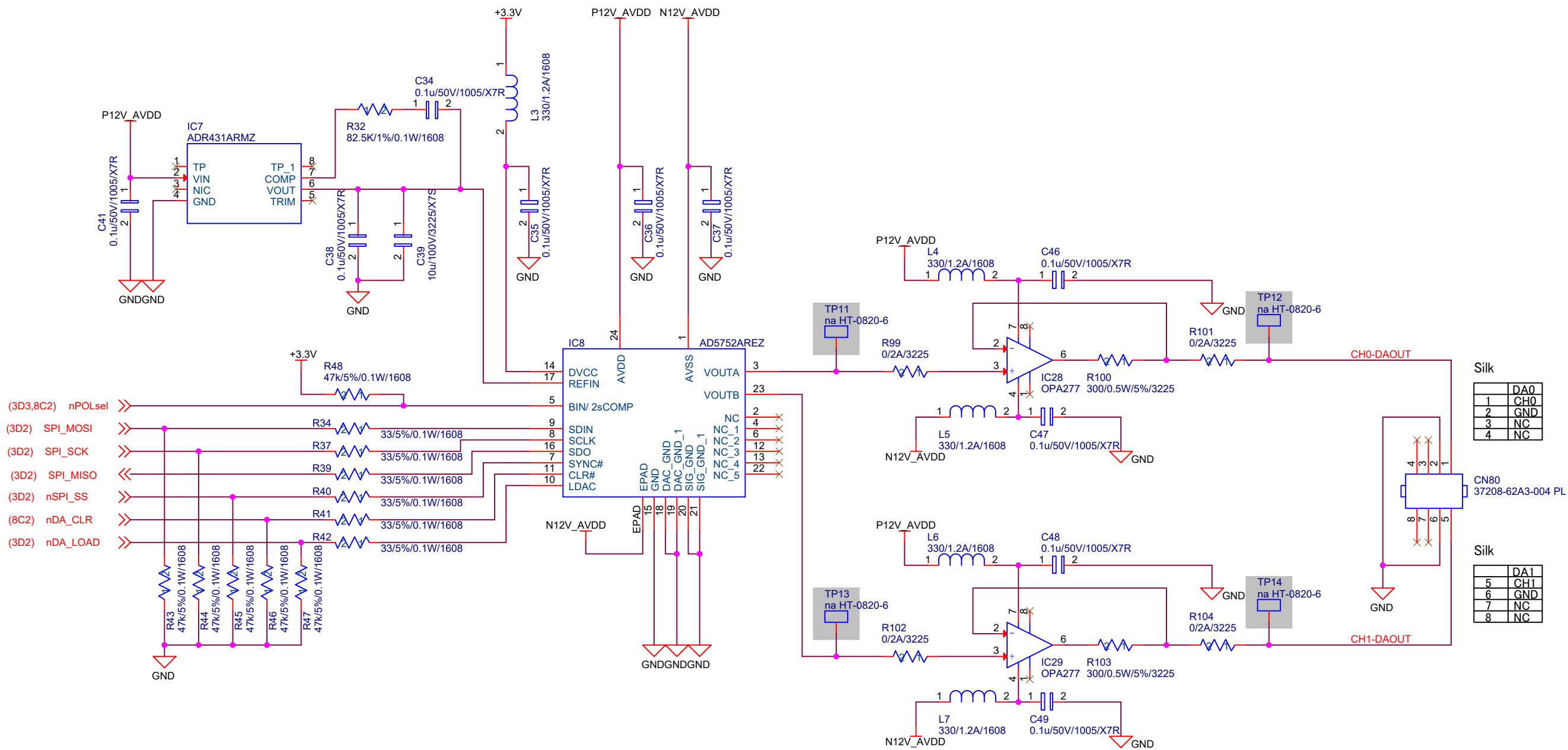


(TP1,TP2は基板の対角に1個ずつ配置する)

Sym	Revision	ECO.No.	Date	Revised	Approved

Design	M. Adachi	DRAW No.	P10464-200
Checked	H. Miyamoto	DRAW Name	IB-44DA16A Circuit Diagram
Approved	T. Maeda	Date	Monday, April 08, 2024
			A3



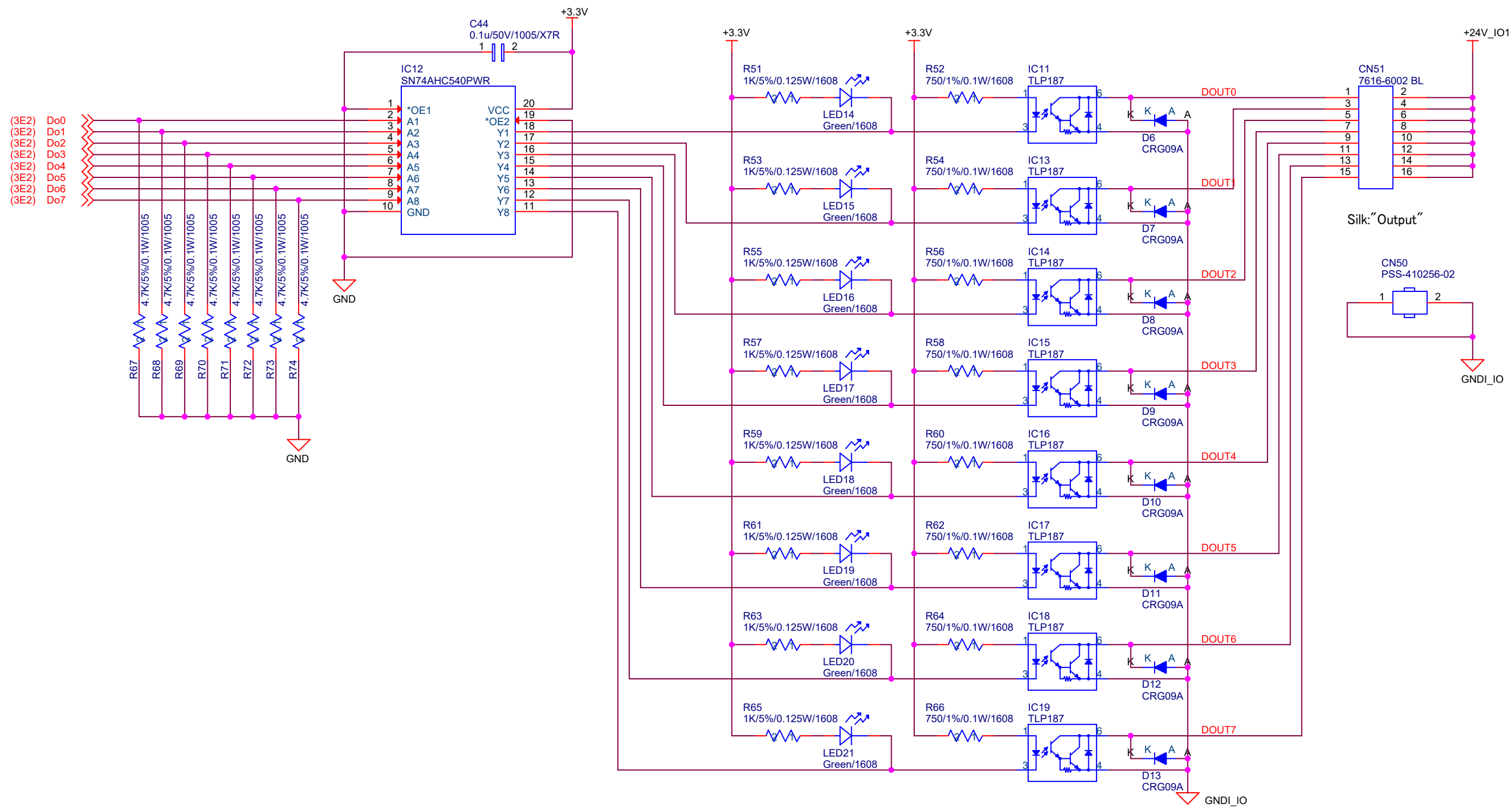


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Sheet 4 / 6

						Design	M. Adachi	DRAW No.	P10464-200	
						Checked	H. Miyamoto	DRAW Name	IB-44DA16A Circuit Diagram	
						Approved	T. Maeda	Date	Monday, April 08, 2024	A3
Sym	Revision	ECO.No.	Date	Revised	Approved					

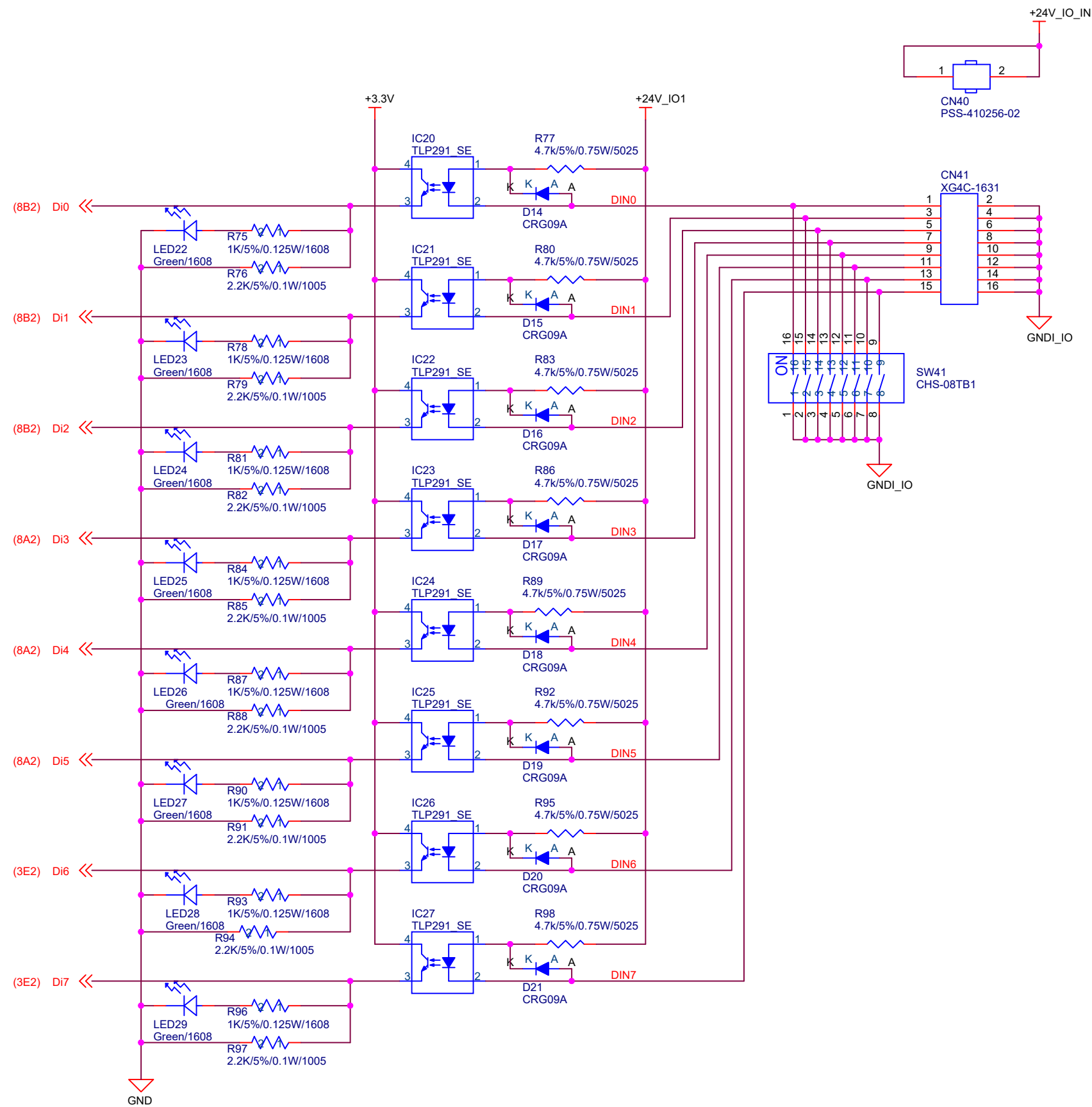


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Sheet 5 / 6

						Design	M. Adachi	DRAW No.	P10464-200
						Checked	H. Miyamoto	DRAW Name	IB-44DA16A Circuit Diagram
						Approved	T. Maeda	Date	Monday, April 08, 2024
Sym	Revision	ECO.No.	Date	Revised	Approved				A3



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						Design	M. Adachi	DRAW No.	P10464-200	
						Checked	H. Miyamoto	DRAW Name	IB-44DA16A Circuit Diagram	
						Approved	T. Maeda	Date	Monday, April 08, 2024	A3
Sym	Revision	ECO.No.	Date	Revised	Approved					