

IB-44AD16A Circuit Diagram

Document No. P10456

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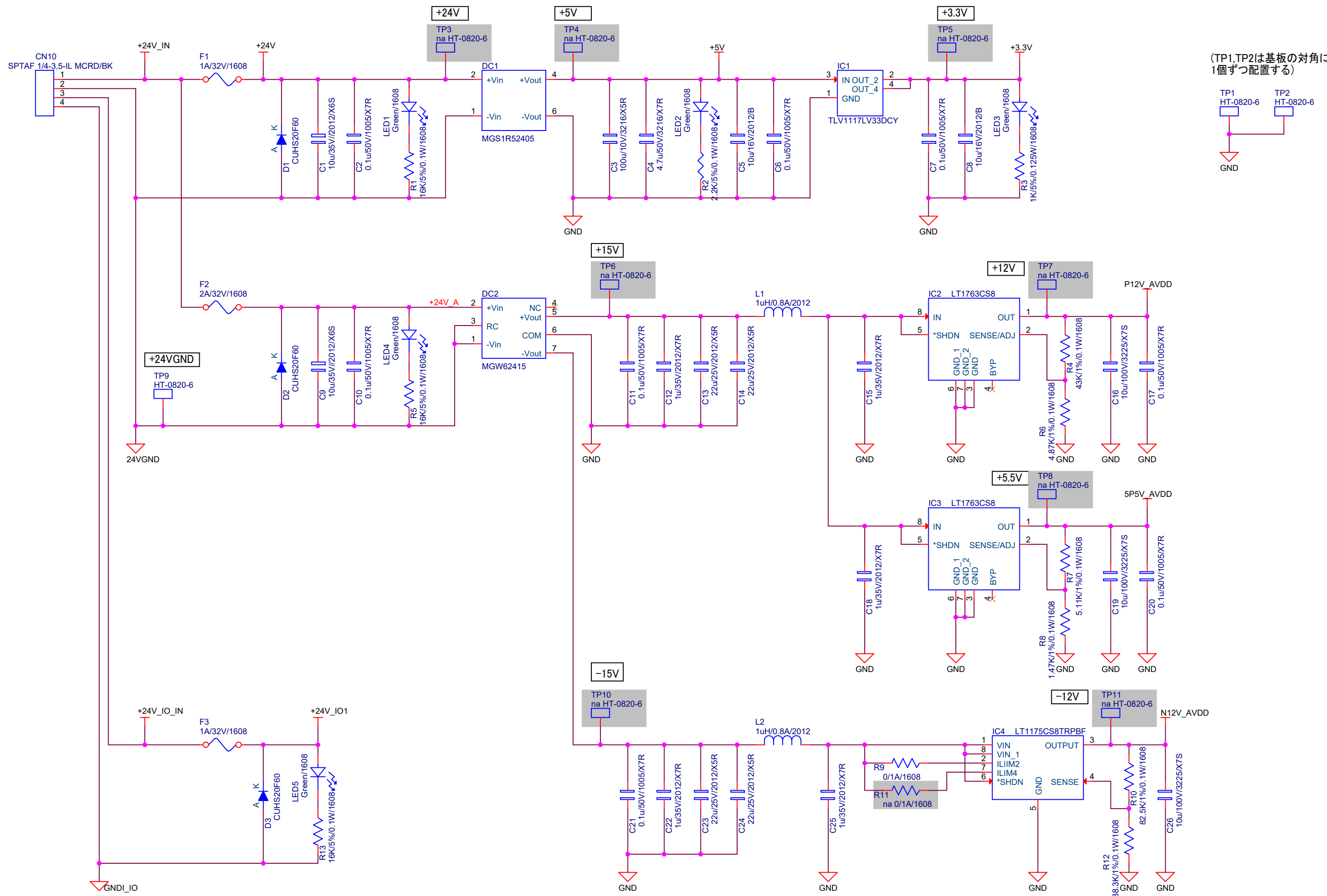
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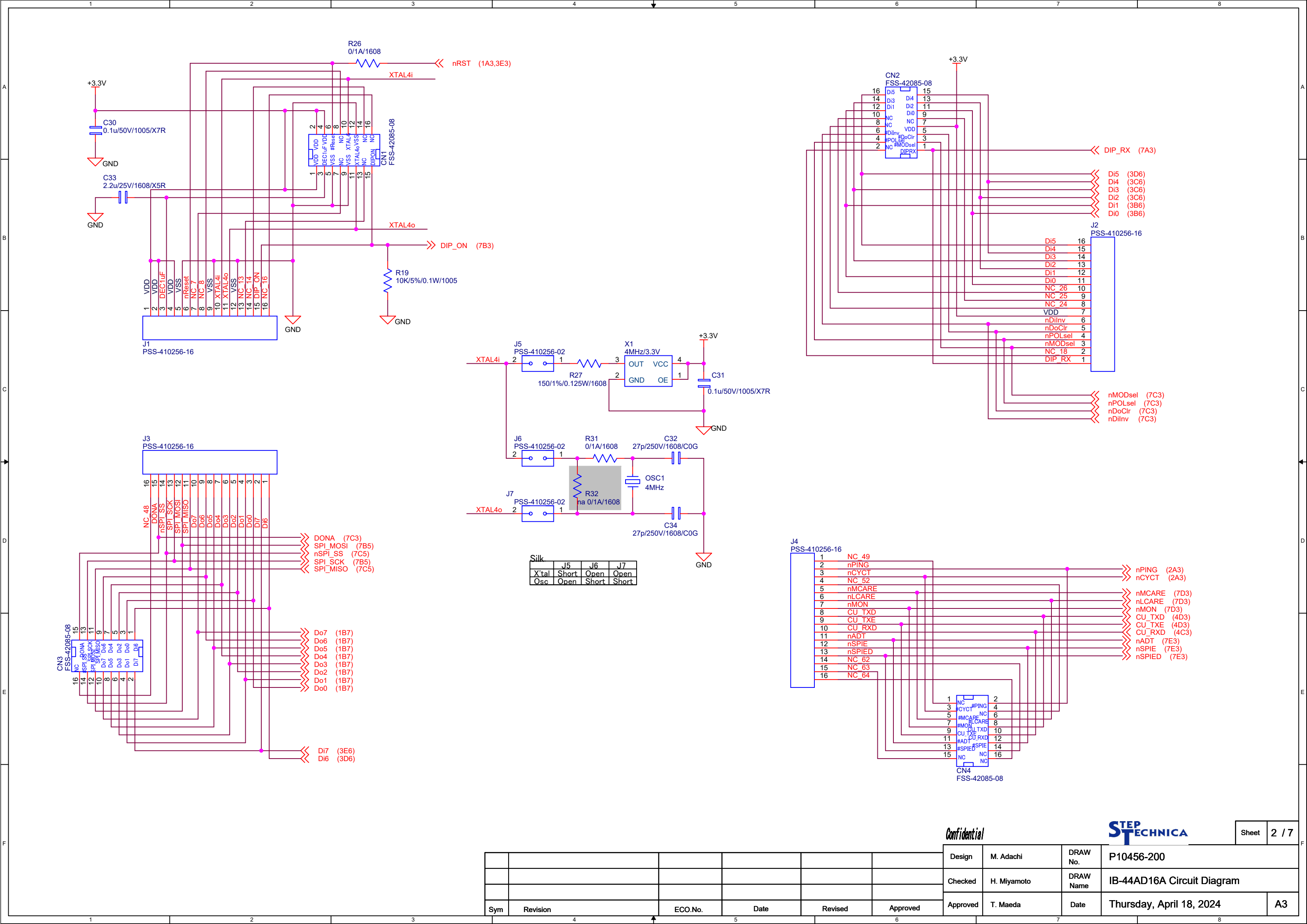
Revision History

[illegible]



(TP1,TP2は基板の対角に1個ずつ配置する)

Sym	Revision	ECO.No.	Date	Revised	Approved



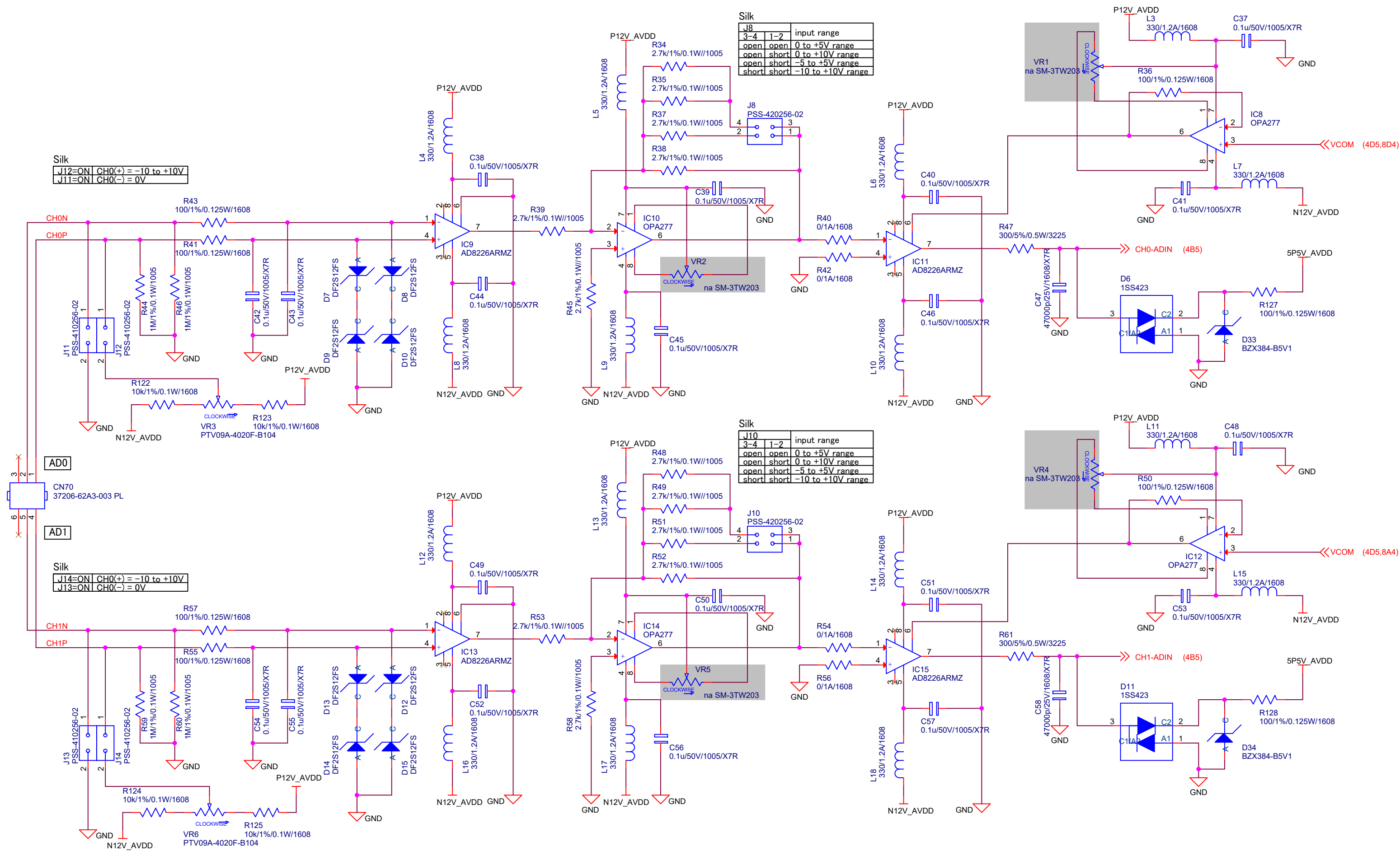
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						Design	M. Adachi	DRAW No.	P10456-200
						Checked	H. Miyamoto	DRAW Name	IB-44AD16A Circuit Diagram
						Approved	T. Maeda	Date	Thursday, April 18, 2024
Sym	Revision	ECO.No.	Date	Revised	Approved				A3

Silk
J12=ON | CH0(+) = -10 to +10V
J11=ON | CH0(-) = 0V

J8		input range
3-4	1-2	
open	open	0 to +5V range
open	short	0 to +10V range
open	short	-5 to +5V range
short	short	-10 to +10V range



Silk
J14=ON | CH0(+) = -10 to +10V
J13=ON | CH0(-) = 0V

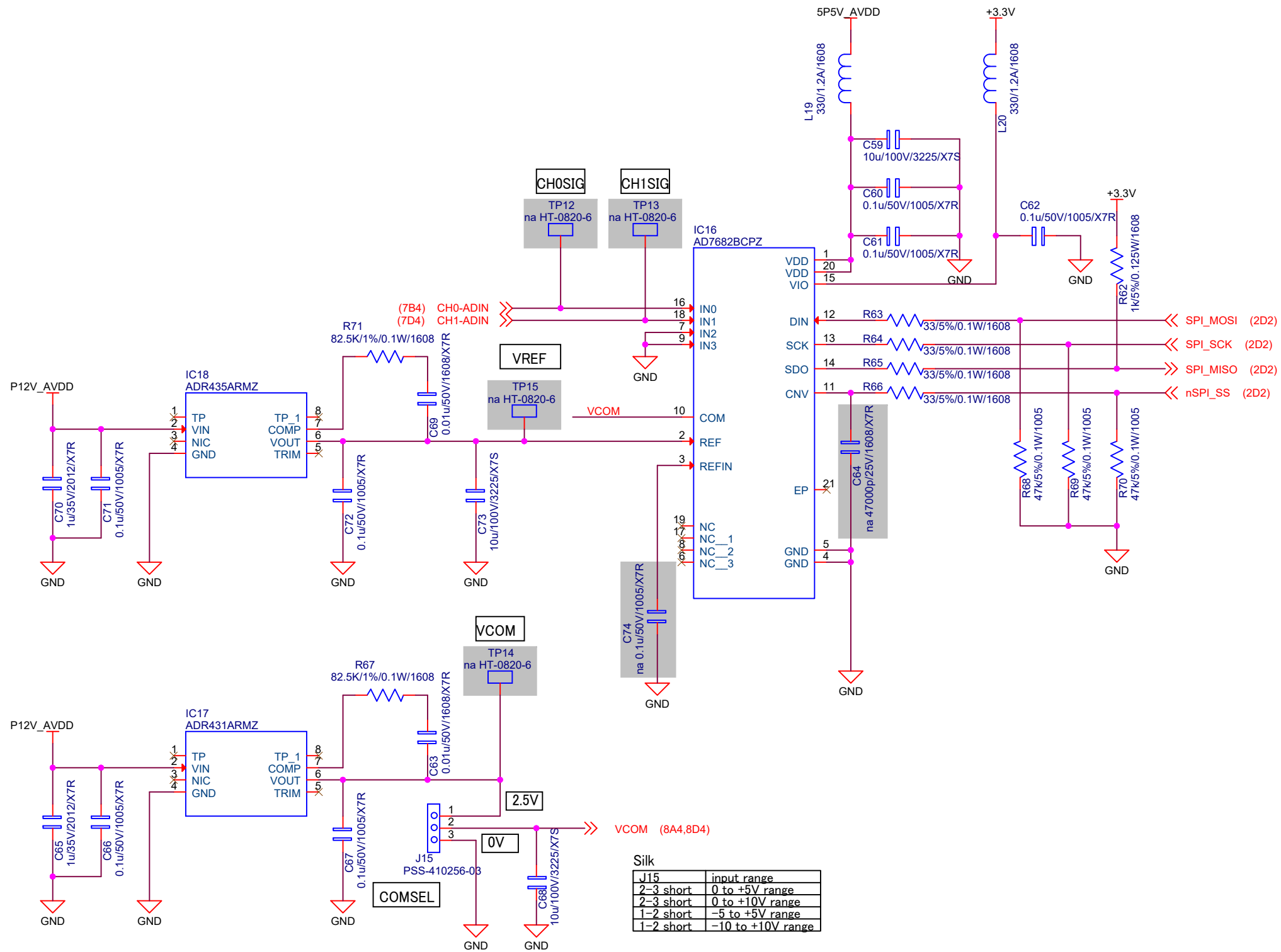
J10		input range
3-4	1-2	
open	open	0 to +5V range
open	short	0 to +10V range
open	short	-5 to +5V range
short	short	-10 to +10V range

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STEP
TECHNICA

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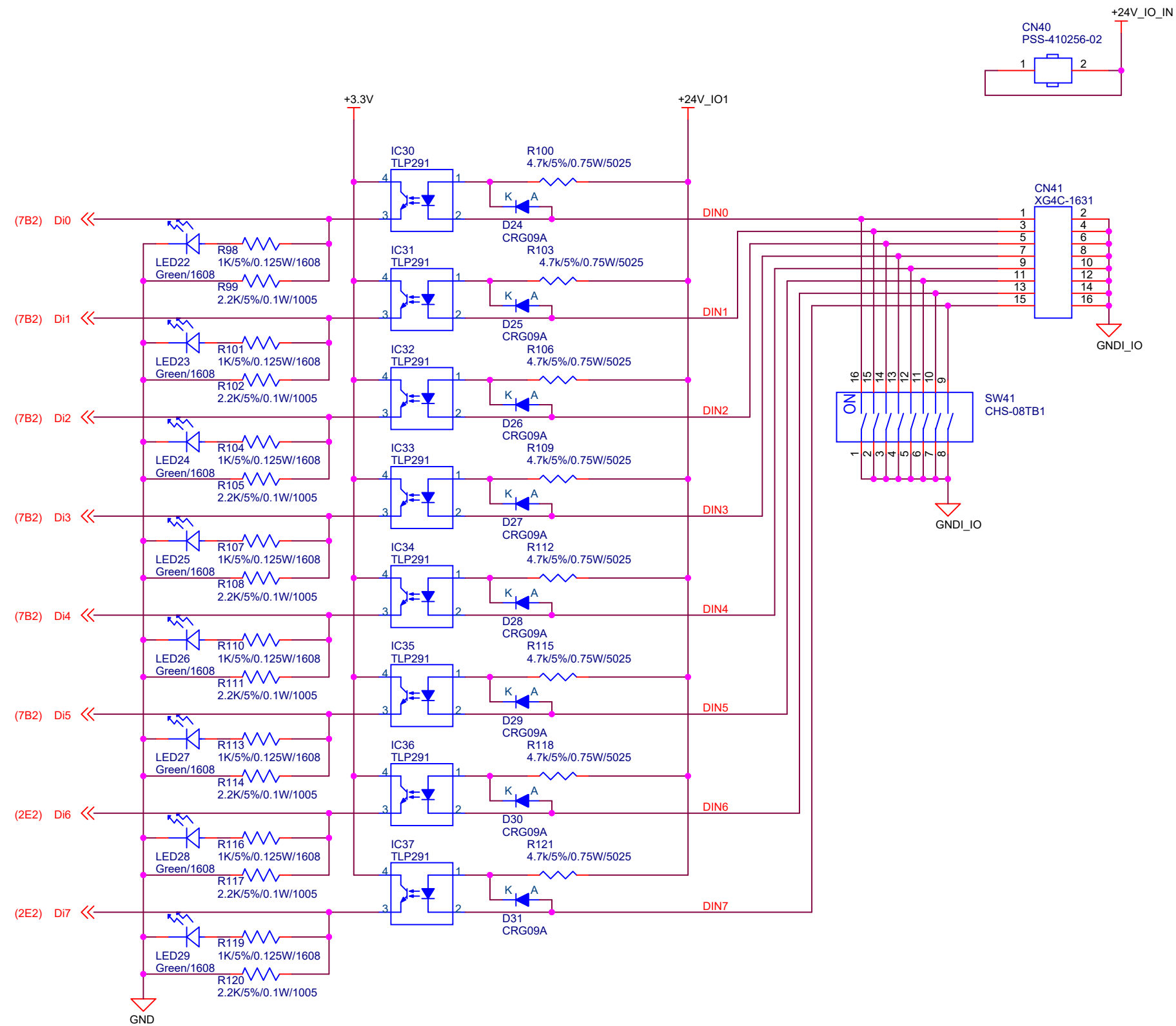
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