

IB-43 Circuit Diagram

Document No. P10436

INDEX

Page1 : Power & Reset Circuit & Trigger Hold Circuit

Page2 : DB board Connection & Clock

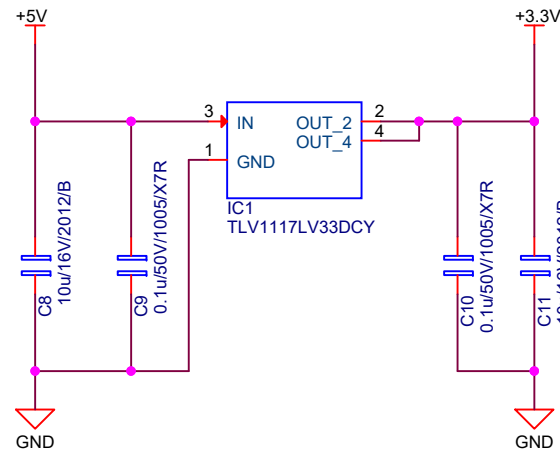
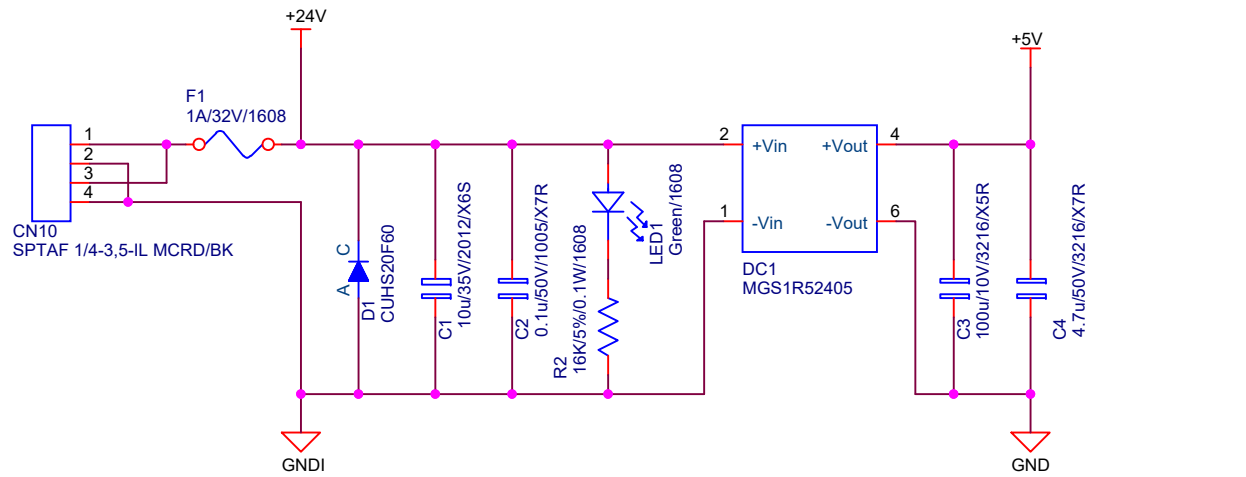
Page3 : RS-485 Interface & Jumper/LED

Page4 : Nucleo Interface

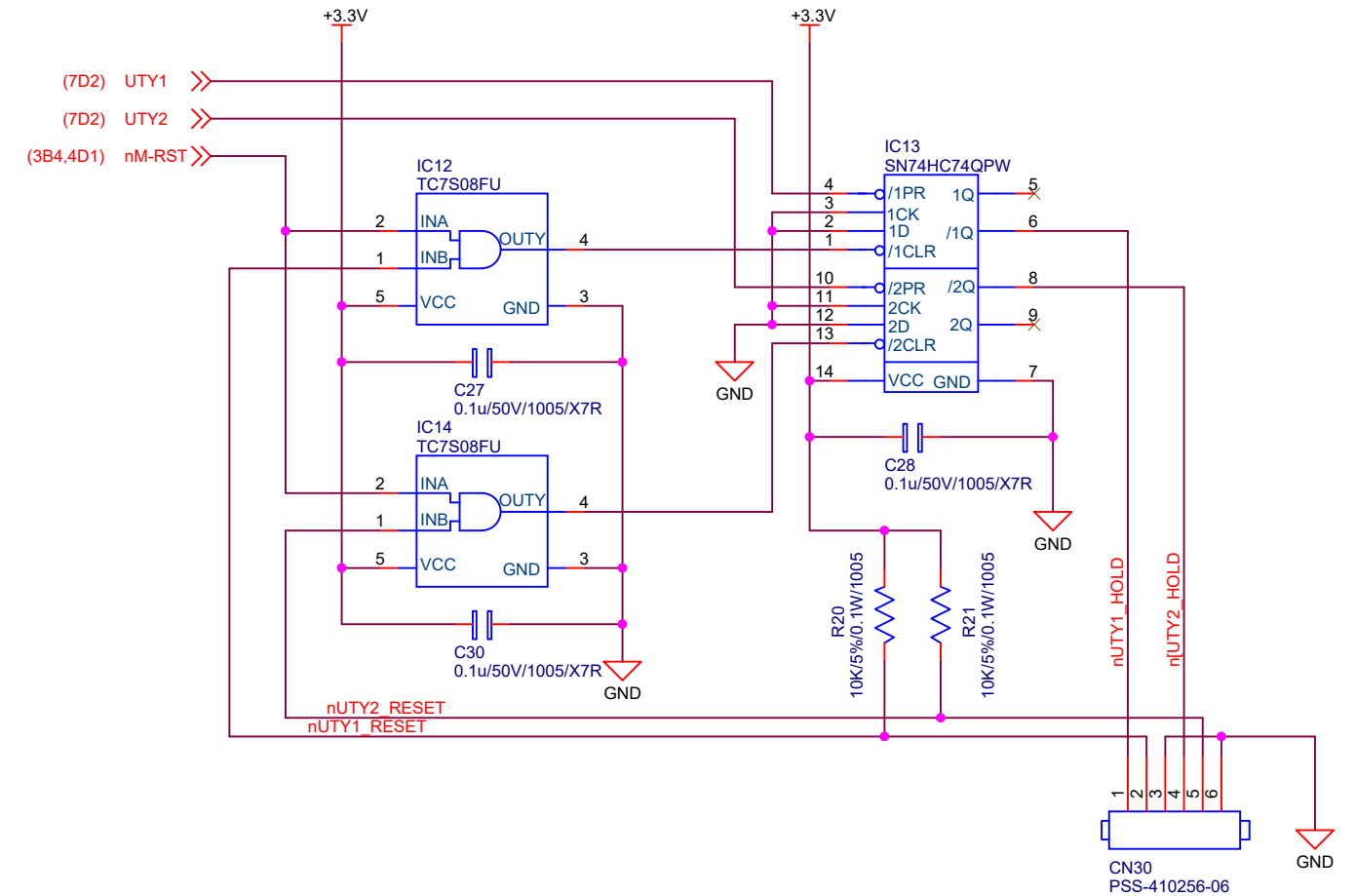
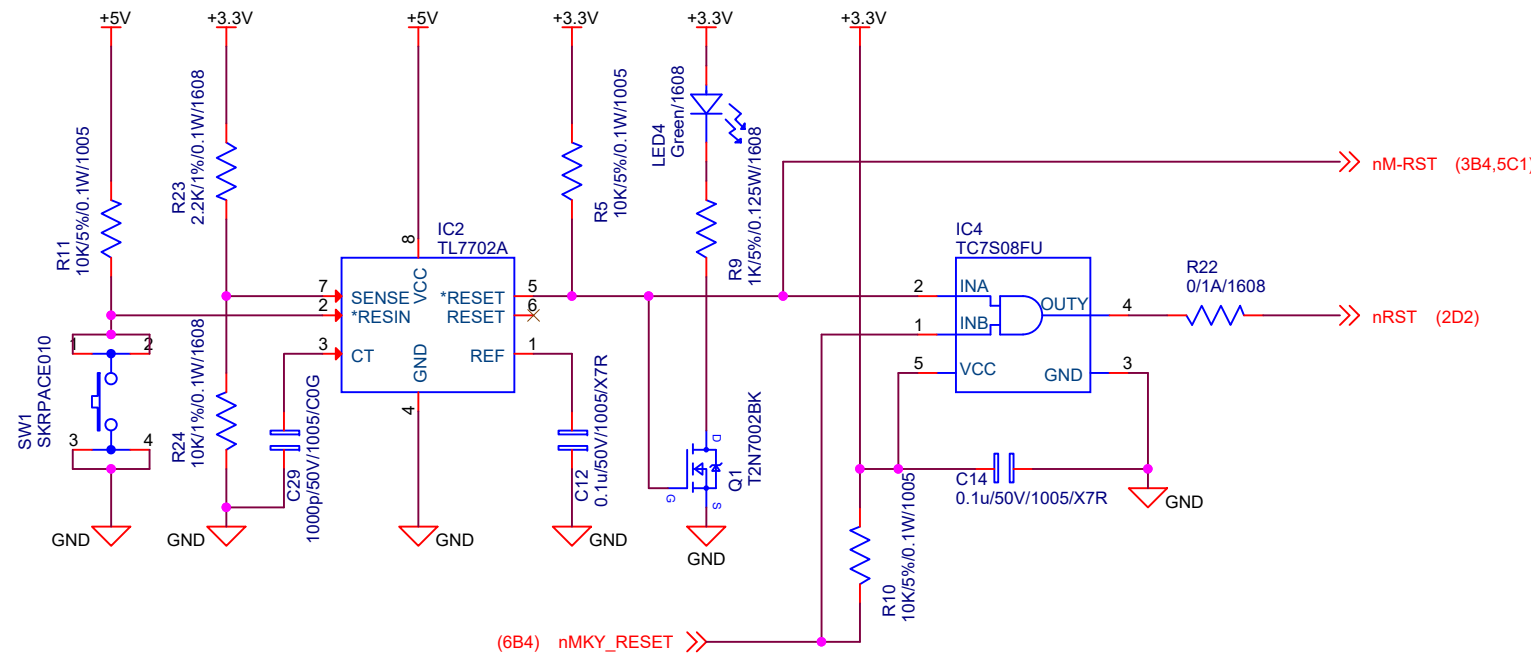
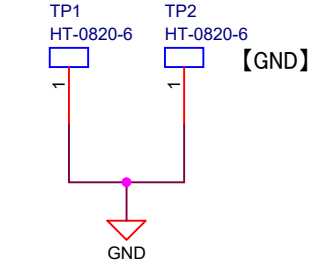
Page5 : Bus Interface

Revision History

[illegible]



(TP1,TP2は基板の対角に1個ずつ配置する)

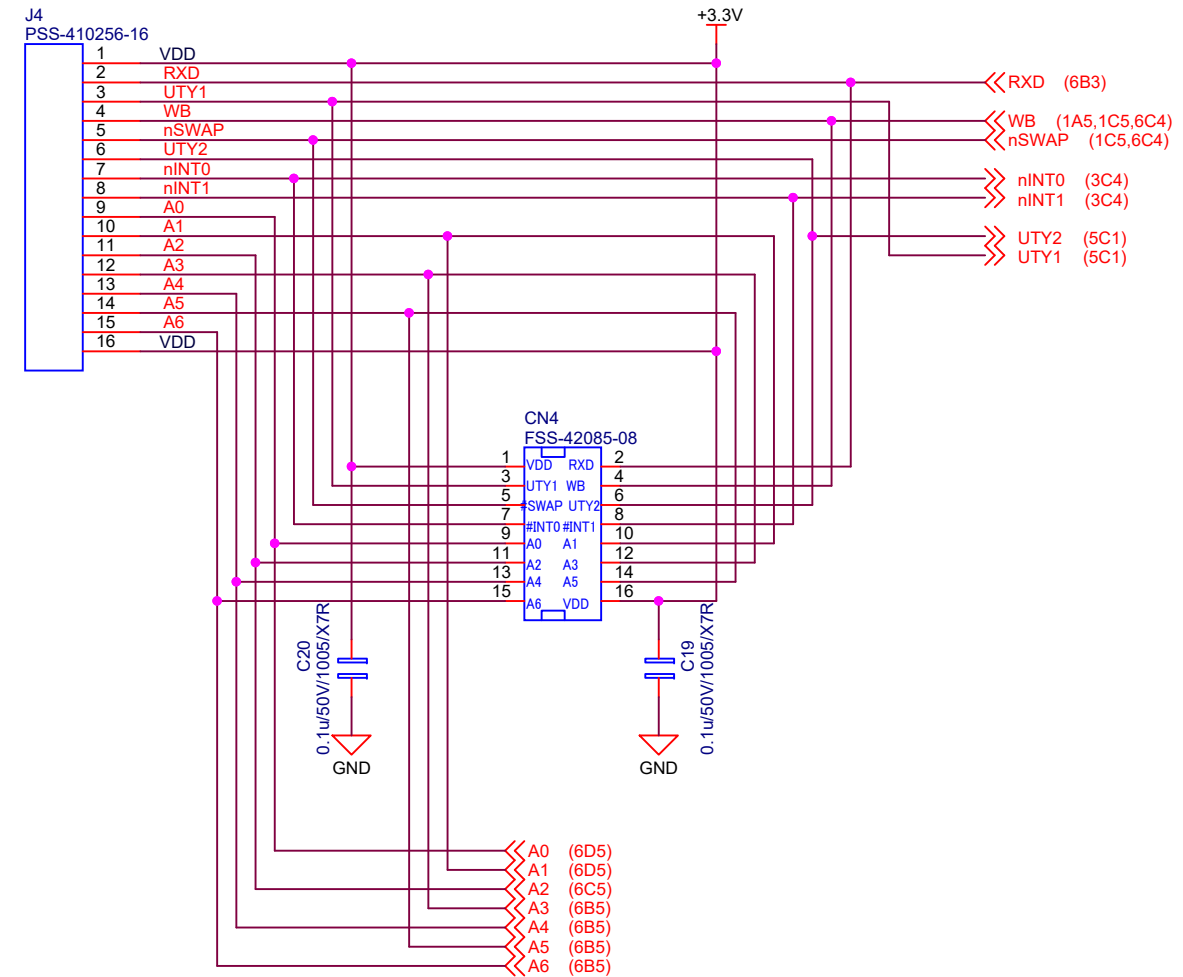
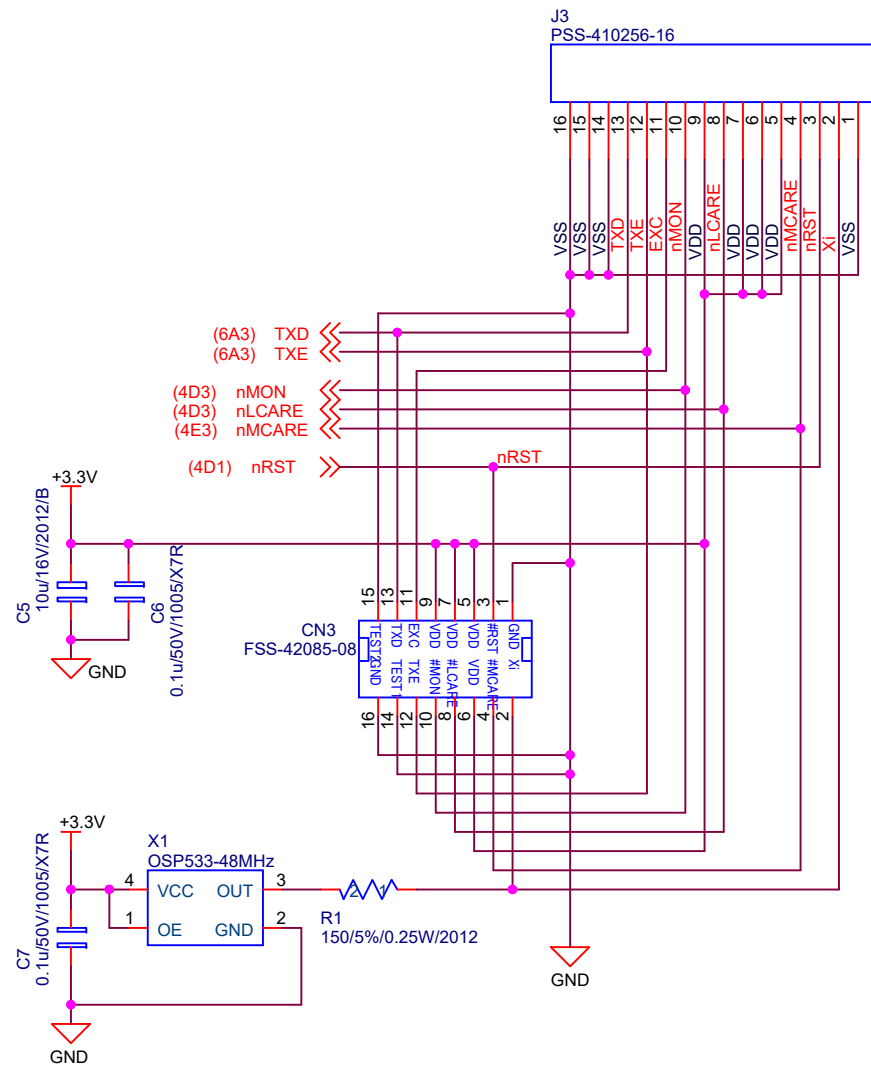
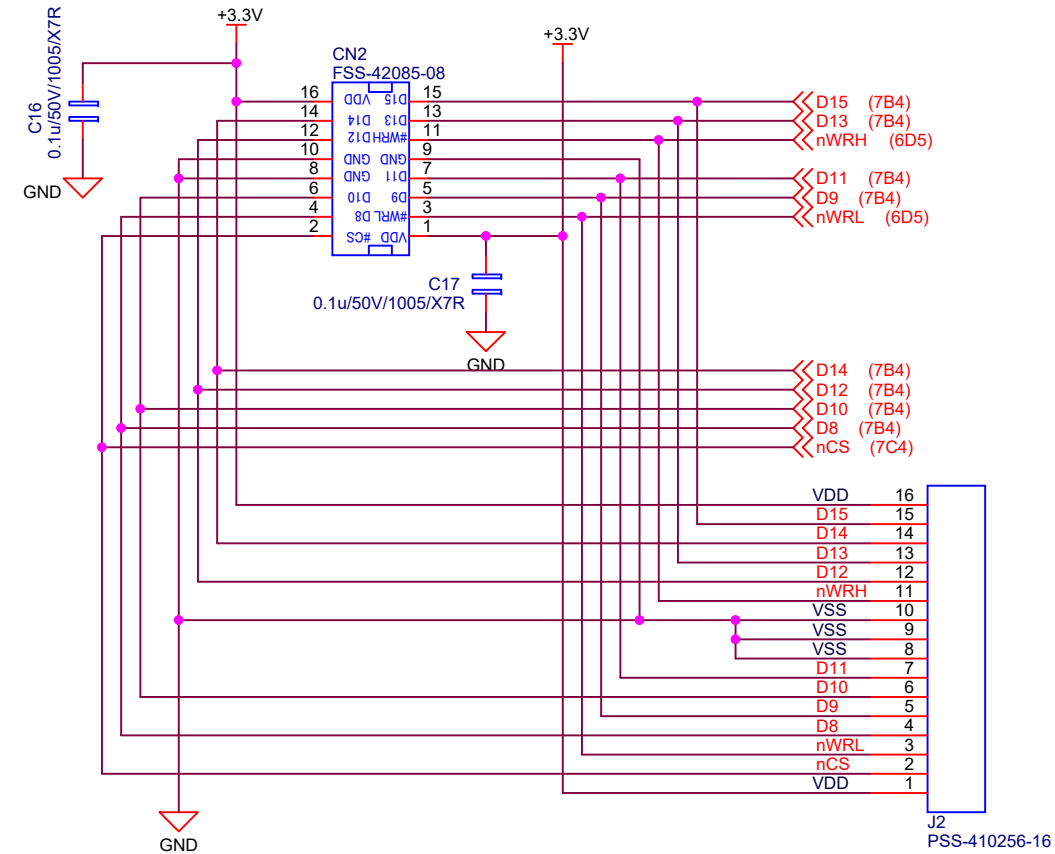
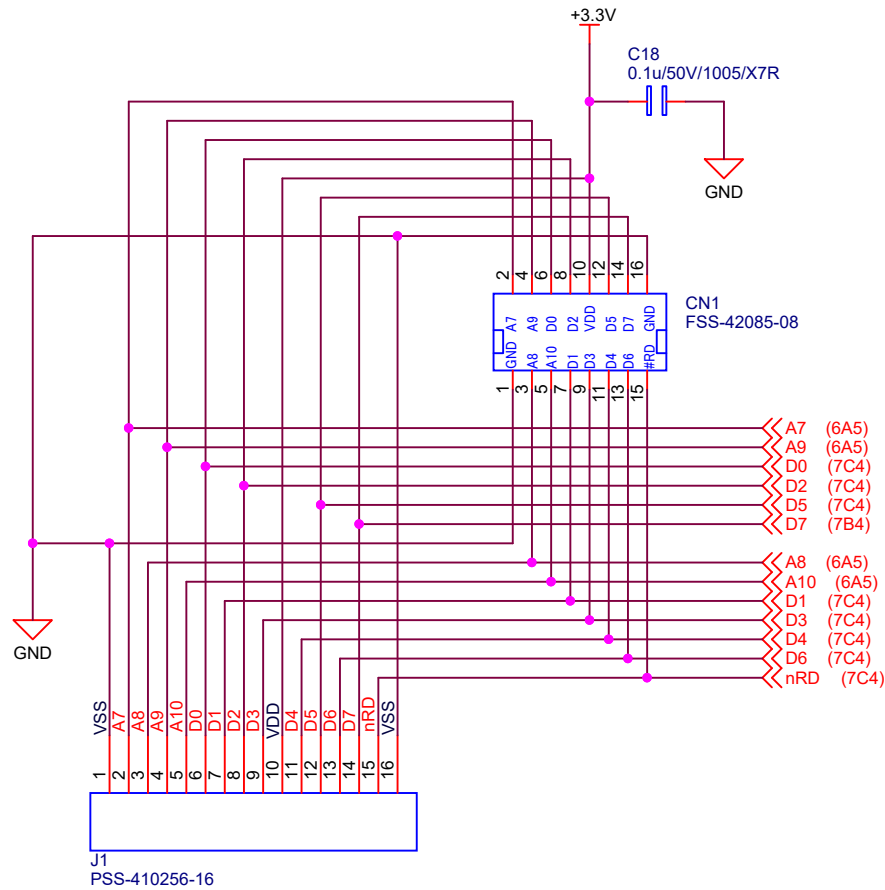


Confidential

STEPTECHNICA

Sheet 1 / 5

						Design	M.Adachi	DRAW No.	P10436-200
						Checked	H.Miyamoto	DRAW Name	IB-43 Circuit Diagram
						Approved	T.Maeda	Date	Tuesday, May 09, 2023
Sym	Revision	ECO.No.	Date	Revised	Approved				A3

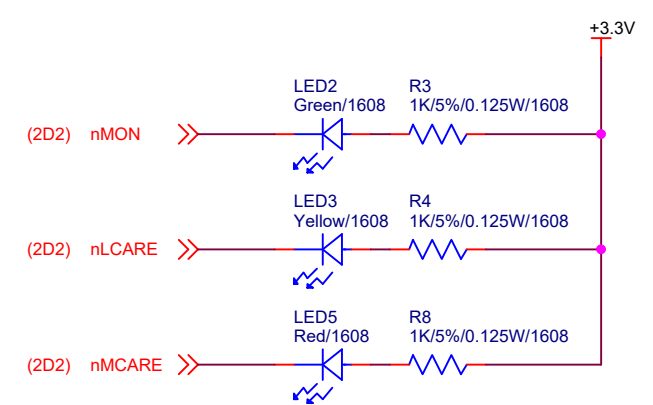
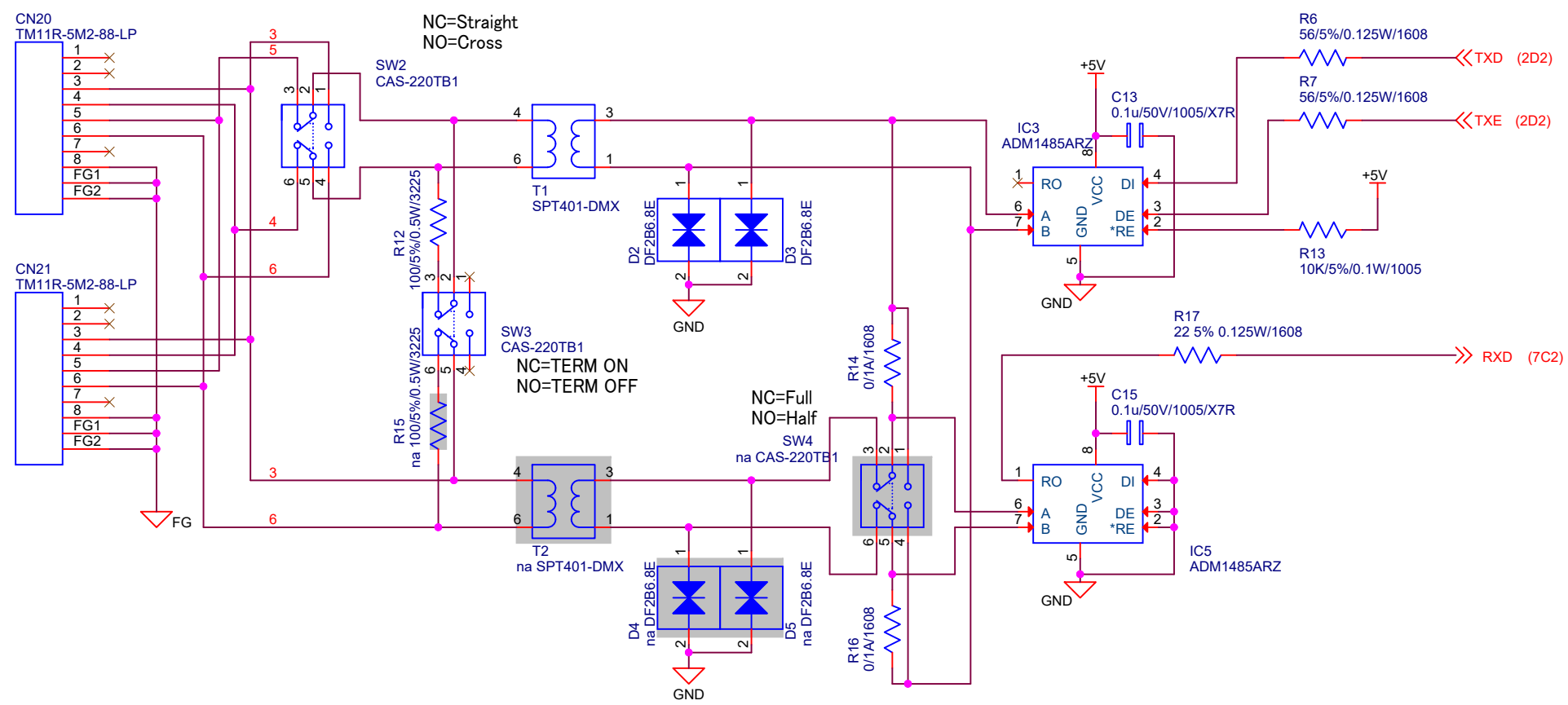


Confidential



Sheet 2 / 5

						Design	M.Adachi	DRAW No.	P10436-200	
						Checked	H.Miyamoto	DRAW Name	IB-43 Circuit Diagram	
						Approved	T.Maeda	Date	Tuesday, May 09, 2023	A3
Sym	Revision	ECO.No.	Date	Revised	Approved					

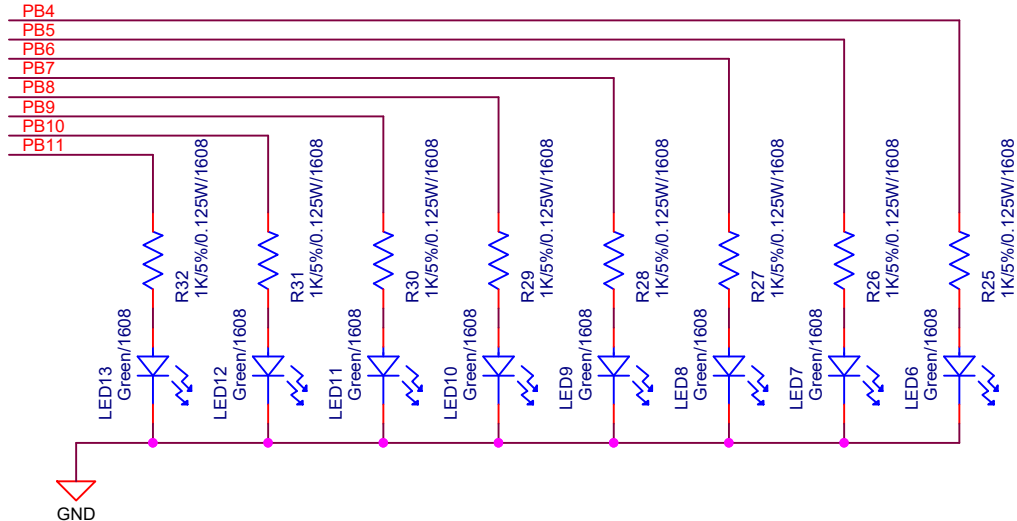
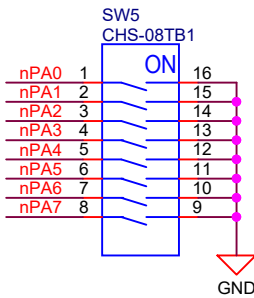
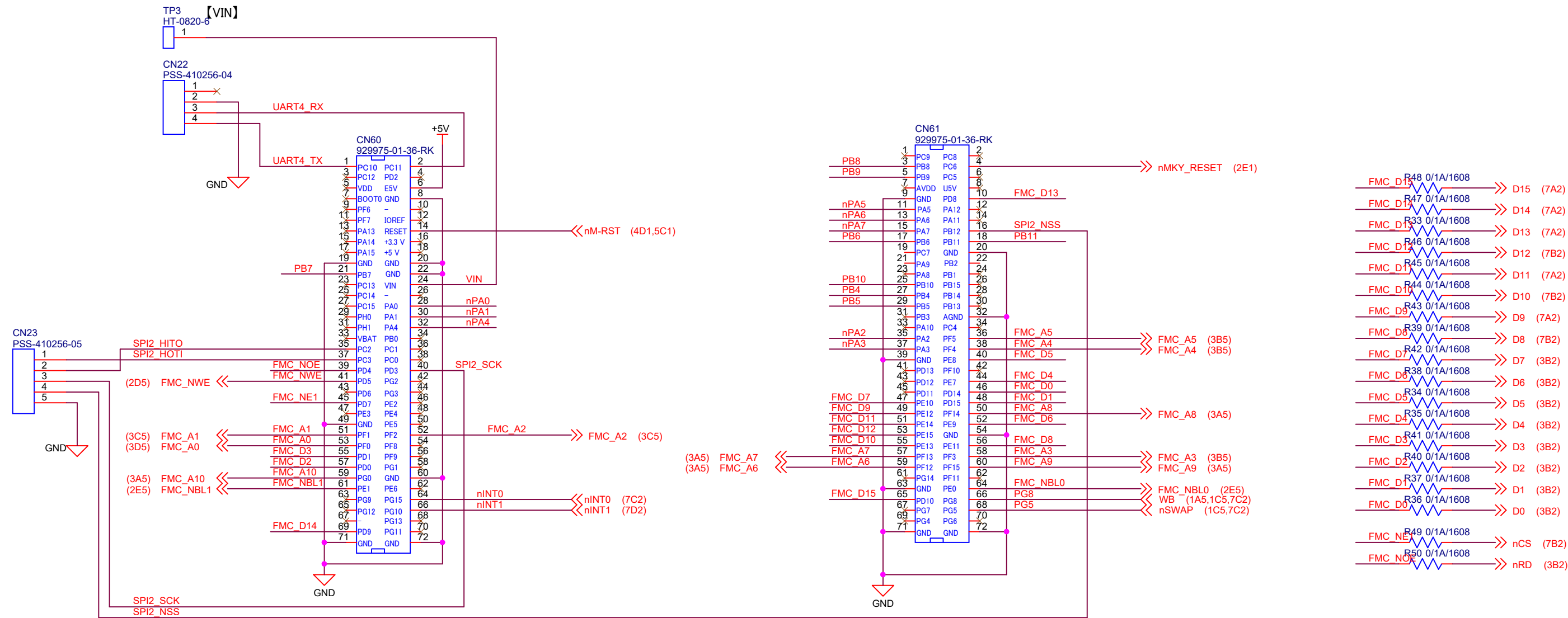


Confidential



						Design	M.Adachi	DRAW No.	P10436-200	
						Checked	H.Miyamoto	DRAW Name	IB-43 Circuit Diagram	
						Approved	T.Maeda	Date	Tuesday, May 09, 2023	A3
Sym	Revision	ECO.No.	Date	Revised	Approved					

(TP3はスルーホールを配置)

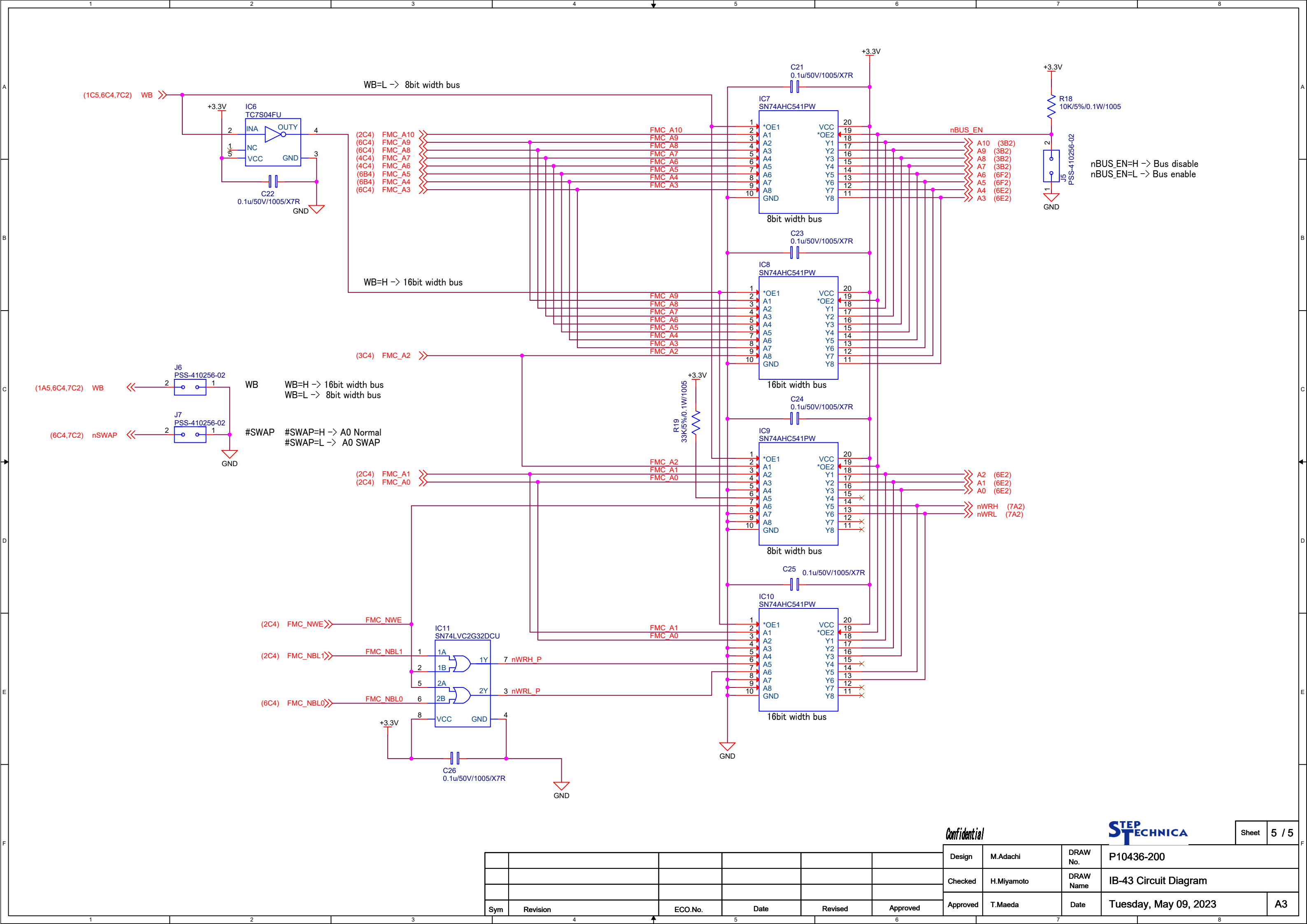


Confidential

STEP
TECHNICA

Sheet 4 / 5

						Design	M.Adachi	DRAW No.	P10436-200
						Checked	H.Miyamoto	DRAW Name	IB-43 Circuit Diagram
						Approved	T.Maeda	Date	Tuesday, May 09, 2023
Sym	Revision	ECO.No.	Date	Revised	Approved				A3



Confidential



Sym	Revision	ECO.No.	Date	Revised	Approved

Design	M.Adachi	DRAW No.	P10436-200		
Checked	H.Miyamoto	DRAW Name	IB-43 Circuit Diagram		
Approved	T.Maeda	Date	Tuesday, May 09, 2023	A3	