

IB-36 Circuit Diagram

Document No. P10444

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Page2 : DB board Connection & Clock

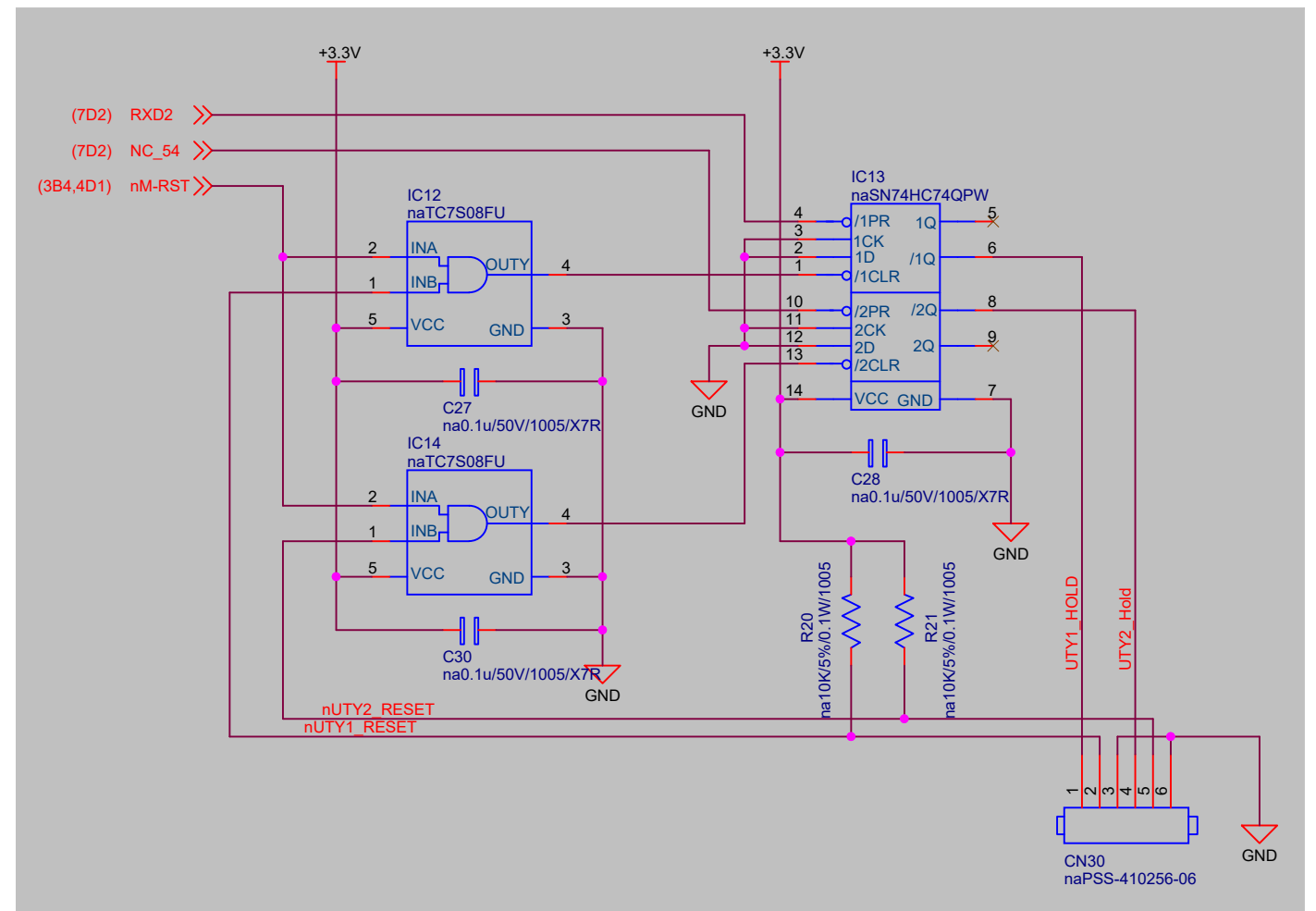
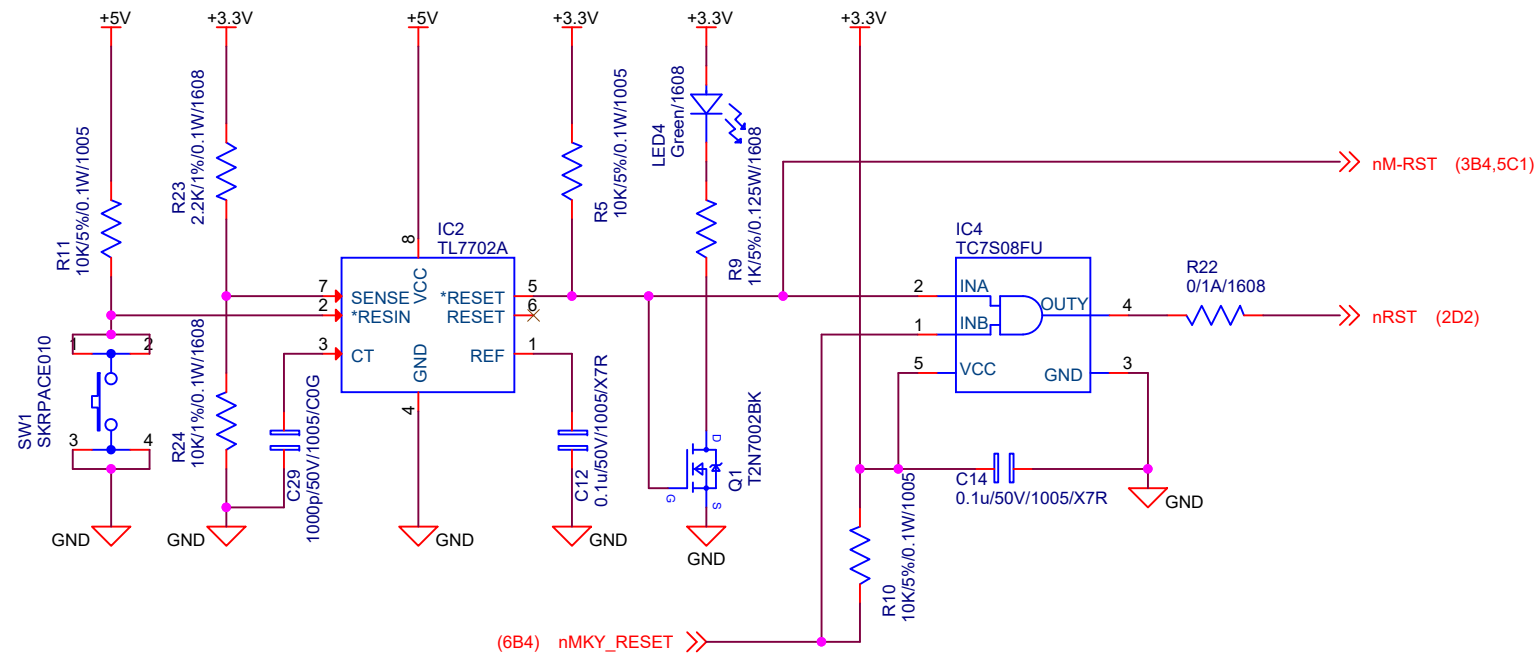
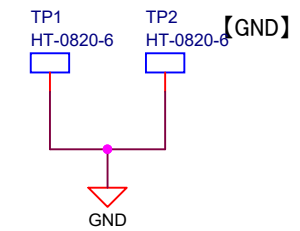
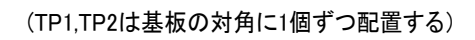
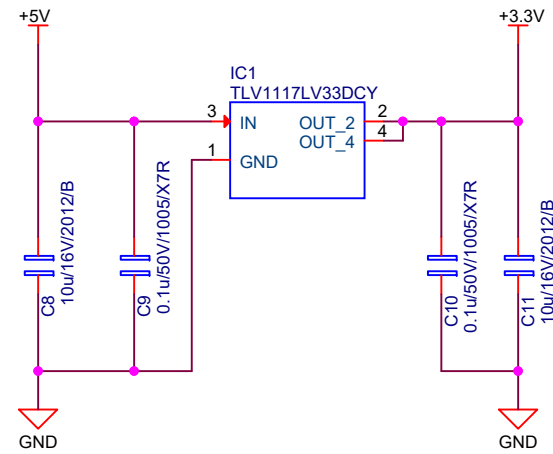
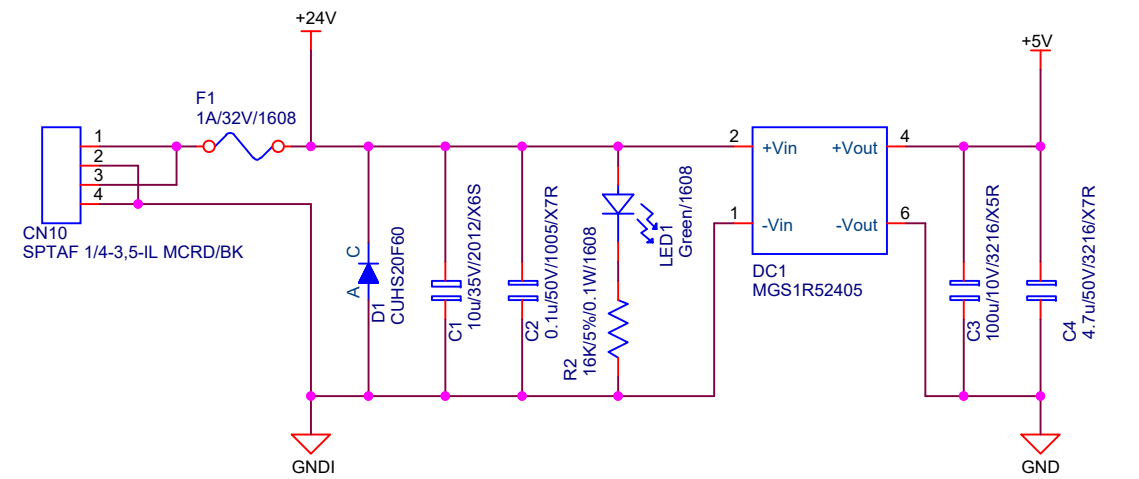
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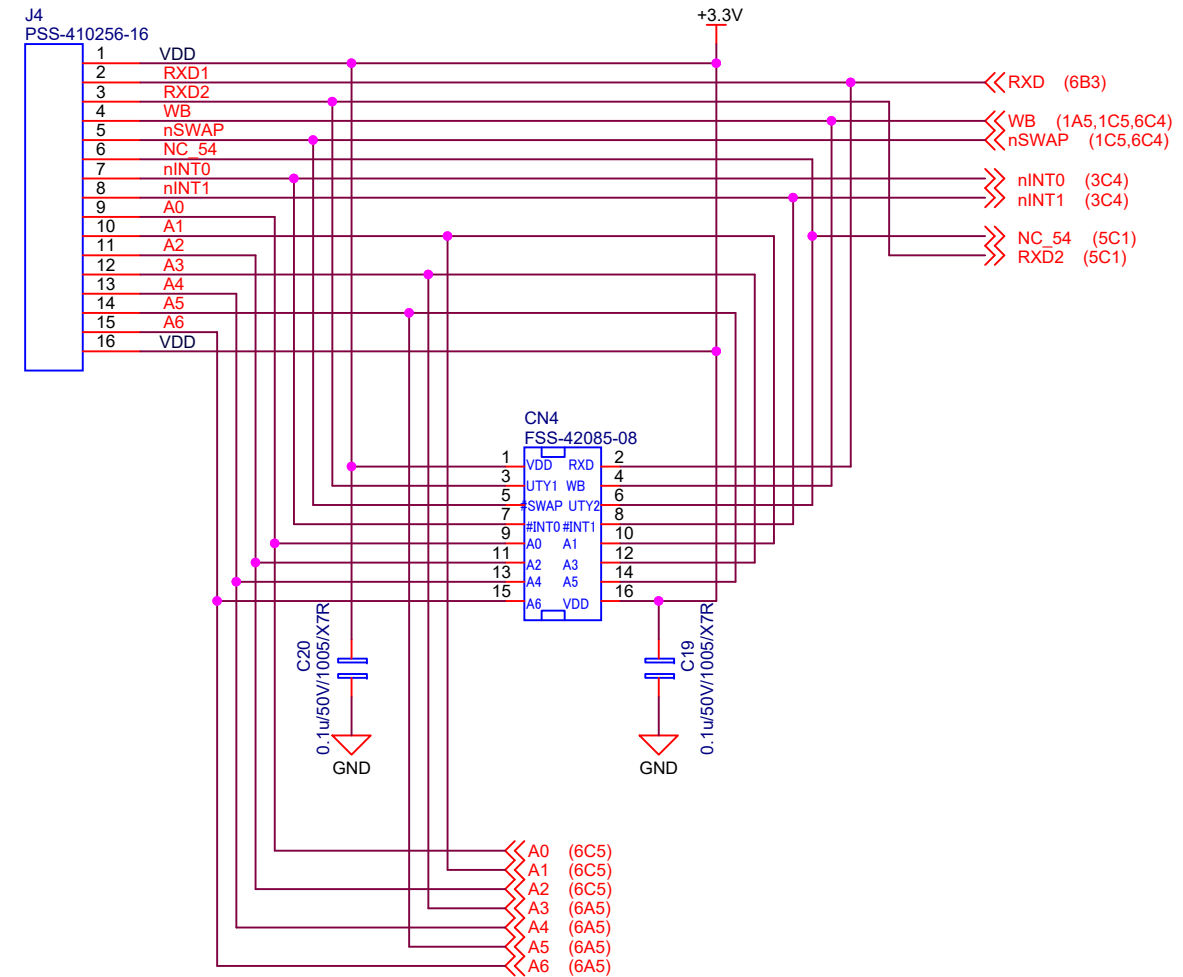
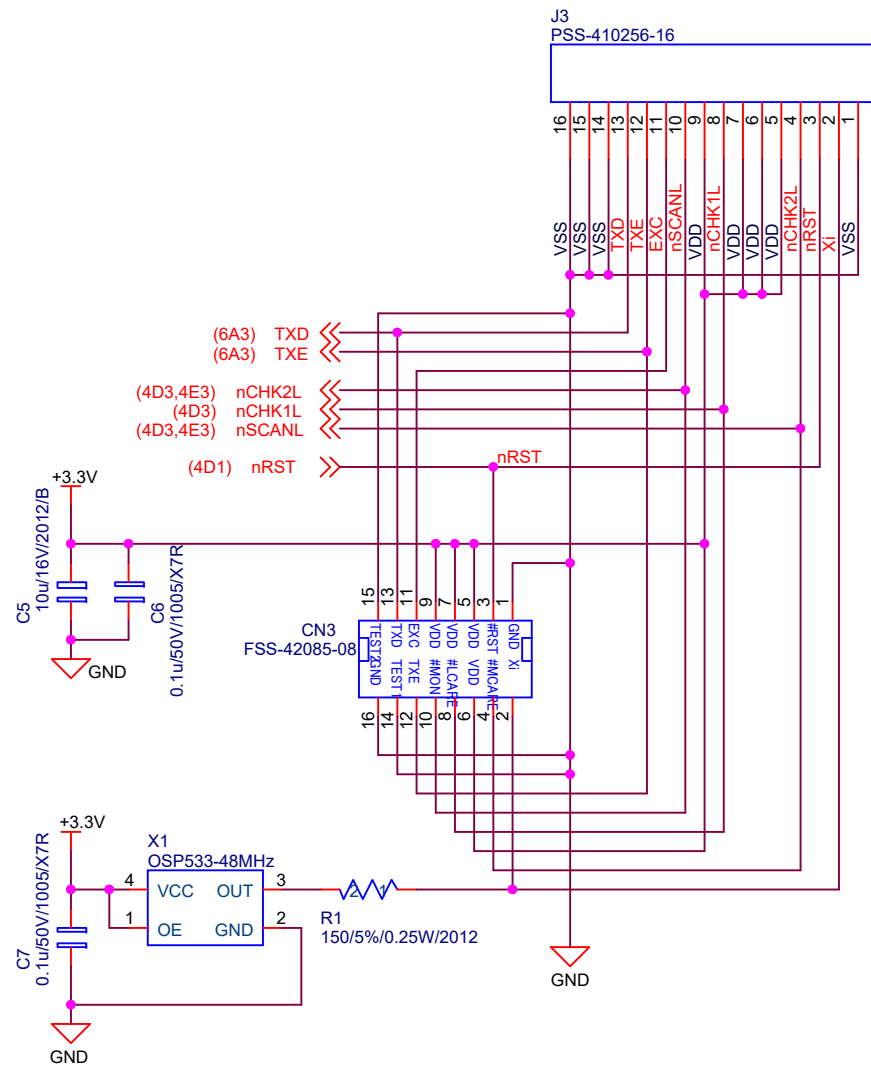
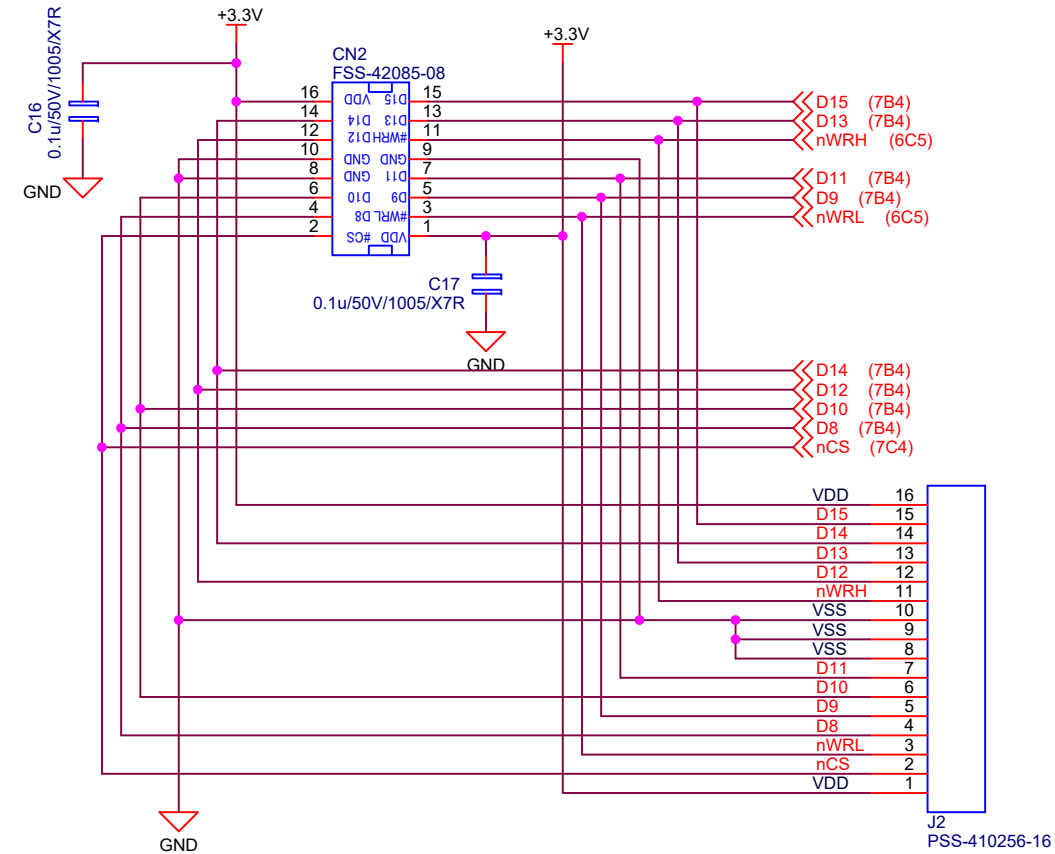
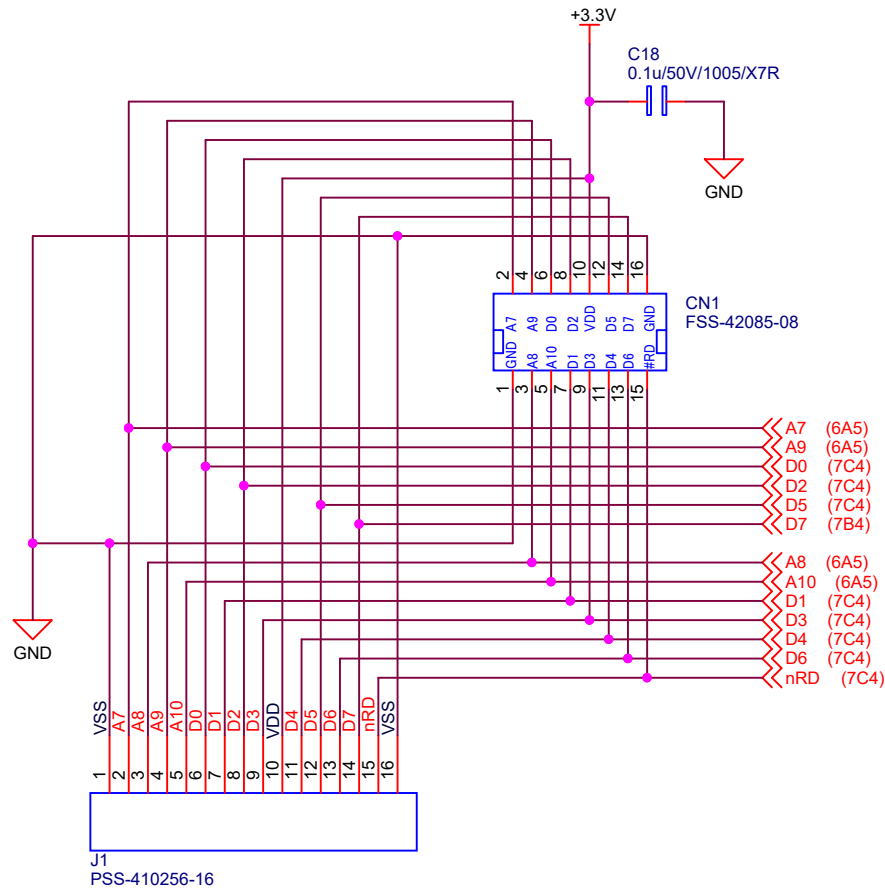
Page5 : Bus Interface

Revision History

[illegible]



						Design	M.Adachi	DRAW No.	P10444-200	
						Checked	H.Miyamoto	DRAW Name	IB-36 Circuit Diagram	
						Approved	T.Maeda	Date	Tuesday, May 09, 2023	A3

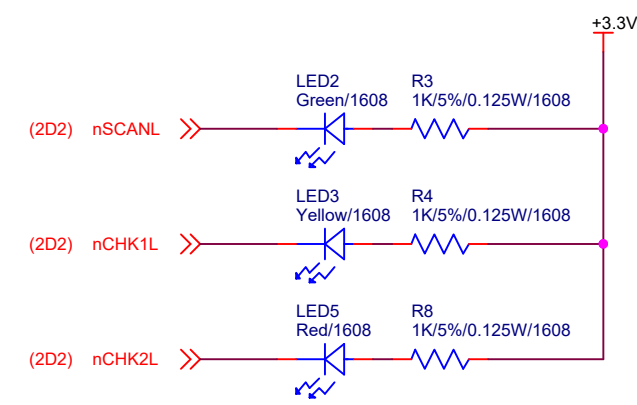
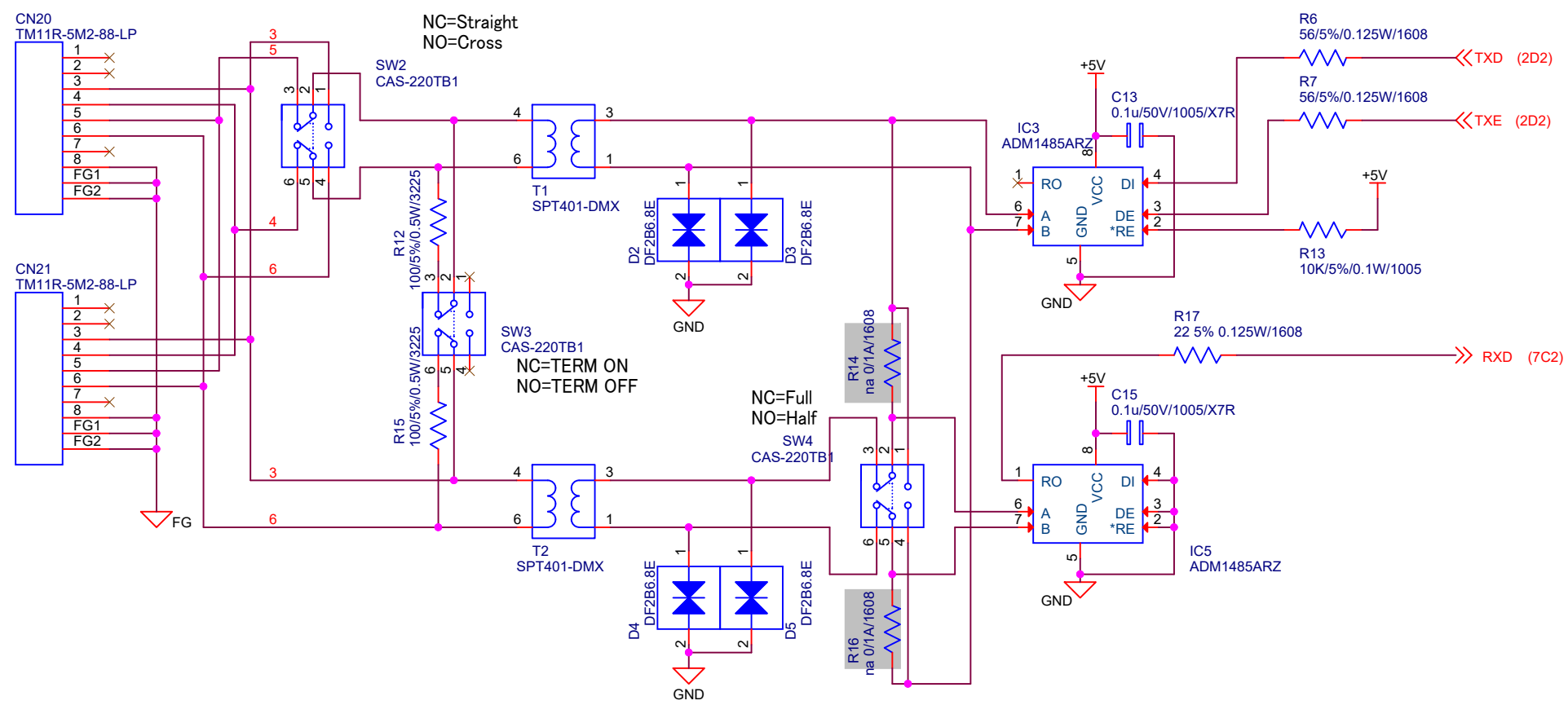


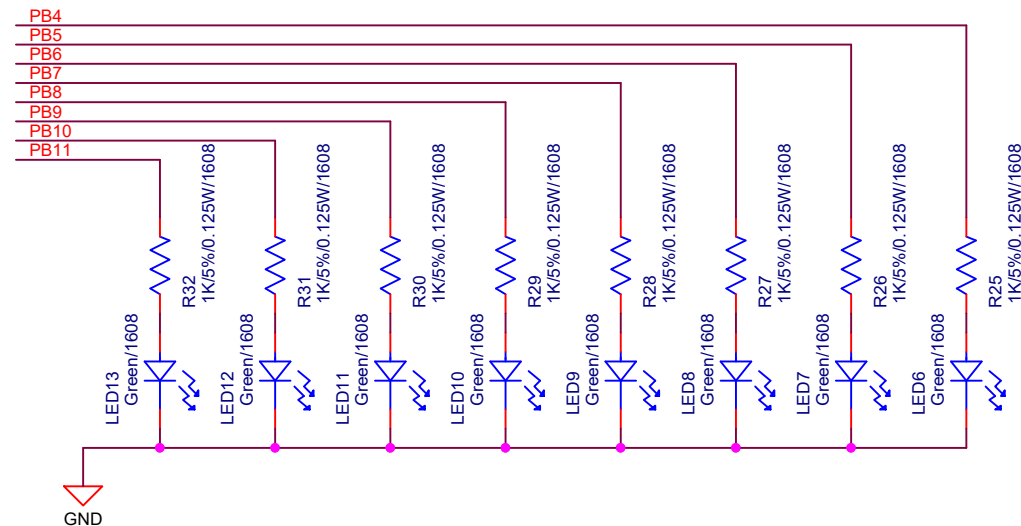
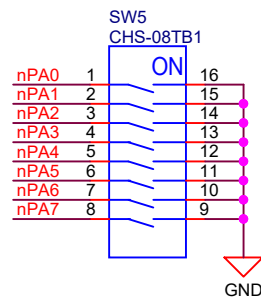
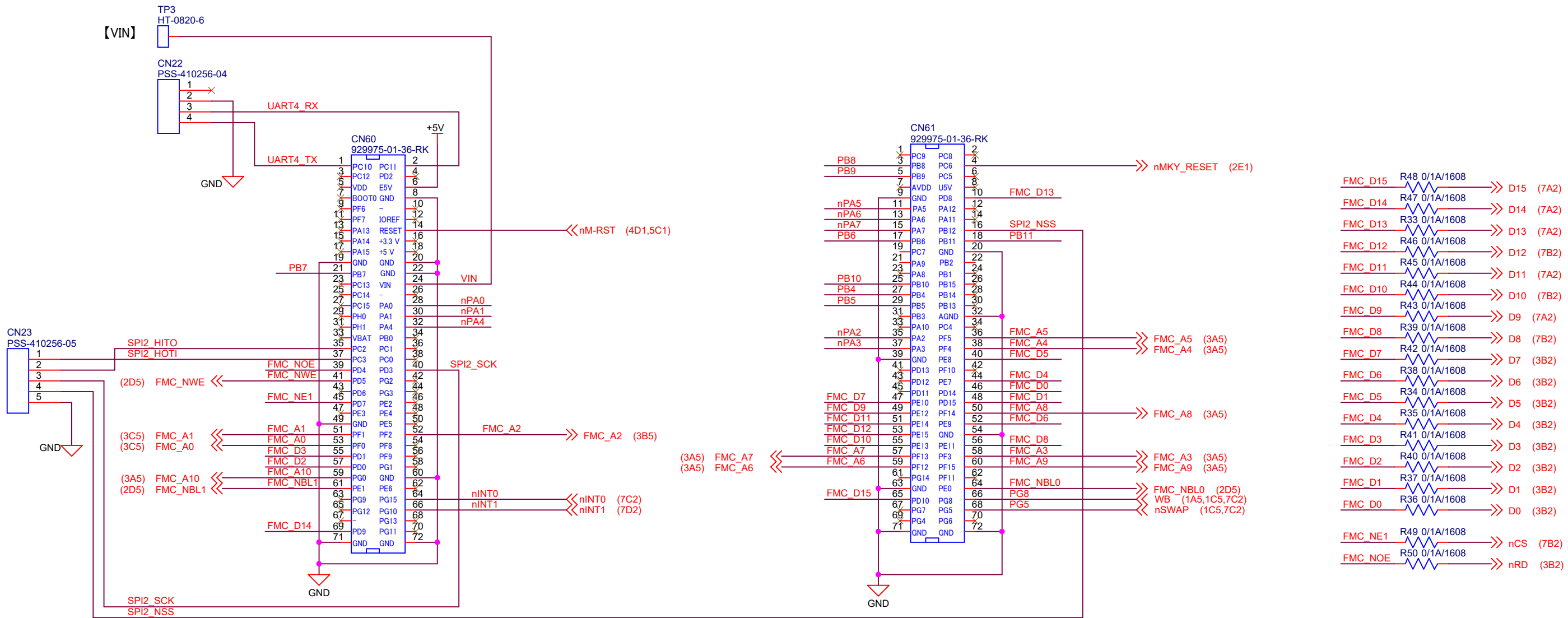
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						Design	M.Adachi	DRAW No.	P10444-200
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						Approved	T.Maeda	Date	Tuesday, May 09, 2023
Sym	Revision	ECO.No.	Date	Revised	Approved				A3



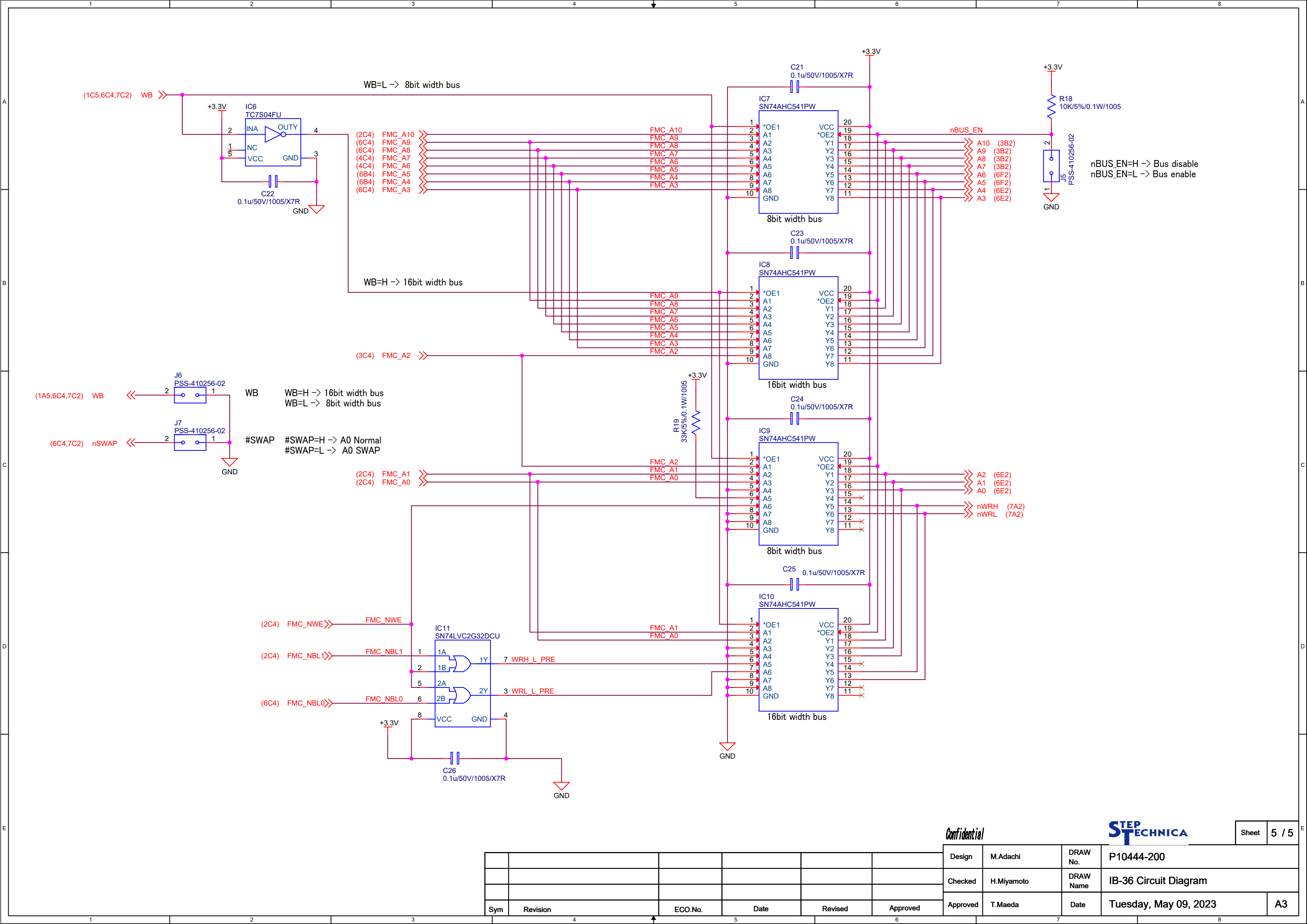


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STEP
TECHNICA

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						Design	M.Adachi	DRAW No.	P10444-200
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