Output state of each pin when power is applied to MKY33, MKY34, MKY35

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■Product

 MKY33, MKY34, MKY35

■Description

The output states of each pin right after power-on of MKY33, MKY34 and MKY35 are listed below. The initial value stated here is applied only when the reset signal in accordance with the description given in the User's Manual is involved.

■Output state of each pin of MKY33（right after power-on）

Output signal	Initial value	Note
UD0-15	Hi-Z	
MA0-10	Lo	The immediately preceding output is maintained while reset. Lo is set by cancelling reset.
MD0-7	Hi-Z	
ACK	Lo	The immediately preceding output is maintained while reset. Lo is set by cancelling reset.
DREQ	Lo	
SCANW	Lo	
SCANR	Lo	
CHK1	Lo	
CHK2	Lo	
TXD	Lo	
TXE	Lo	
MRD	Lo	
MWR	Hi	

■Output state of each pin of MKY34（right after power-on）

Output signal	Initial value	Note
DO0-15	Lo	The immediately preceding output is maintained while reset. Lo is set by cancelling reset.
STB1	Lo	
STB2	Lo	
SE	Undefined	Not fixed to Hi or Lo
TXD	Lo	The immediately preceding output is maintained while reset. Lo is set by cancelling reset.
TXE	Lo	The immediately preceding output is maintained while reset. Lo is set by cancelling reset.
C0	XI	XI is output also while reset.
0A0-5	IA0-5 +1	Binary value obtained by adding 1 to the value which is input to IA0-5
C01	Lo	$1/2^{15} \times$ XI after the reset signal has been cancelled
C02	Lo	$1/2^{18} \times$ XI after the reset signal has been cancelled
RXDN	RXD	Theoretical value that has been input to RXD

■Output state of each pin of MKY35（right after power-on）

Output signal	Initial value	Note
C0		When BPSS=Lo, XI is output also while reset. When BPSS=Hi, $1/2 \times$ XI is output after reset has been cancelled.
IO0-15	Lo	When output is performed by IOSO-2
STB1	Lo	
STB2	Lo	
TXD	Hi-Z	
TXE	Lo	The immediately preceding output is maintained while reset. Lo is set by cancelling reset.
MON	Hi	